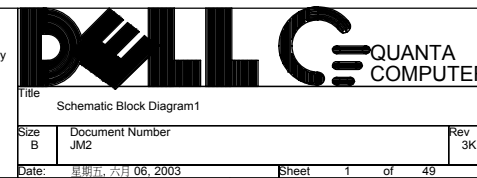
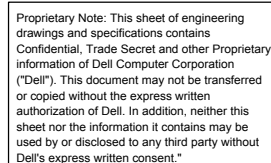
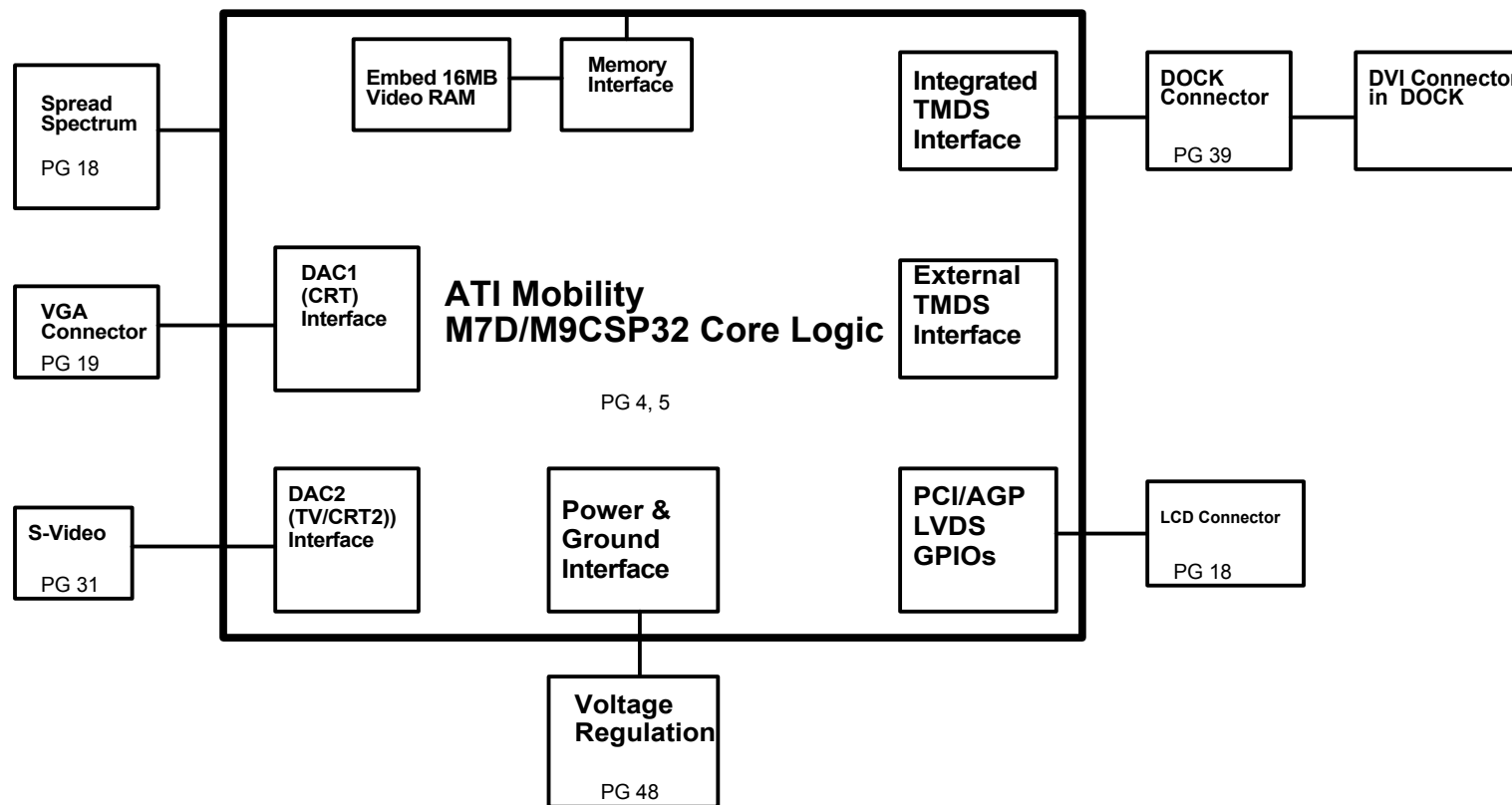


PWA:4U853, Rev A04
SCH: 7U239, Rev A03





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File		Schematic Block Diagram2	
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48	VGA DC/DC	
49	DCIN,Batt	
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Power & Ground			
Label	Pg#	Description	Control Signal
DC_IN+		AC ADAPTER (20V)	
PBATT+		MAIN BATTERY + (10~17V)	
PWR_SRC		MAIN POWER (10~20V)	
RTC_PWR3_3V		RTC & PCL POWER (3_3V)	
+12V		+12V	DRUNPWROK
VHCORE		CPU CORE POWER (1.25/1.15V)	RUNPWROK
V1_2RUN		AGTL+ POWER (1.2V)	RUNPWROK
+3VRUN		SLP_S3# CTRLD POWER	RUN_ON
+3VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VALW		8051 POWER (5V)	
+5VRUN		SLP_S3# CTRLD POWER	RUN_ON
+5VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VHDD		HDD POWER (5V)	HDDC_EN#
+5VMOD		MODULE POWER (5V)	MODC_EN#
STRB#5V		EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1, +5VFAN2		FAN POWER (5V)	FAN_OFF/ON#
VDDA		AUDIO ANALOG POWER (5V)	RUN_ON
1_8VSUS		RESUME WELL IN ICH	
1_8VRUN		SLP_S3# CTRLD POWER	
+3VALW		8051 POWER (3V)	
V1_5RUN		AGP I/O POWER	
 GND	ALL PAGES	DIGITAL GROUND	
 AGND		AUDIO GND	
 GNDP		CPU POWER GND	
 CGNDP		CHARGER GND	
 DGNDP		DC/DC POWER GND	
 LANGND		COMBO CONN GND	

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File

Index, DNI, Power & Ground

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Date:

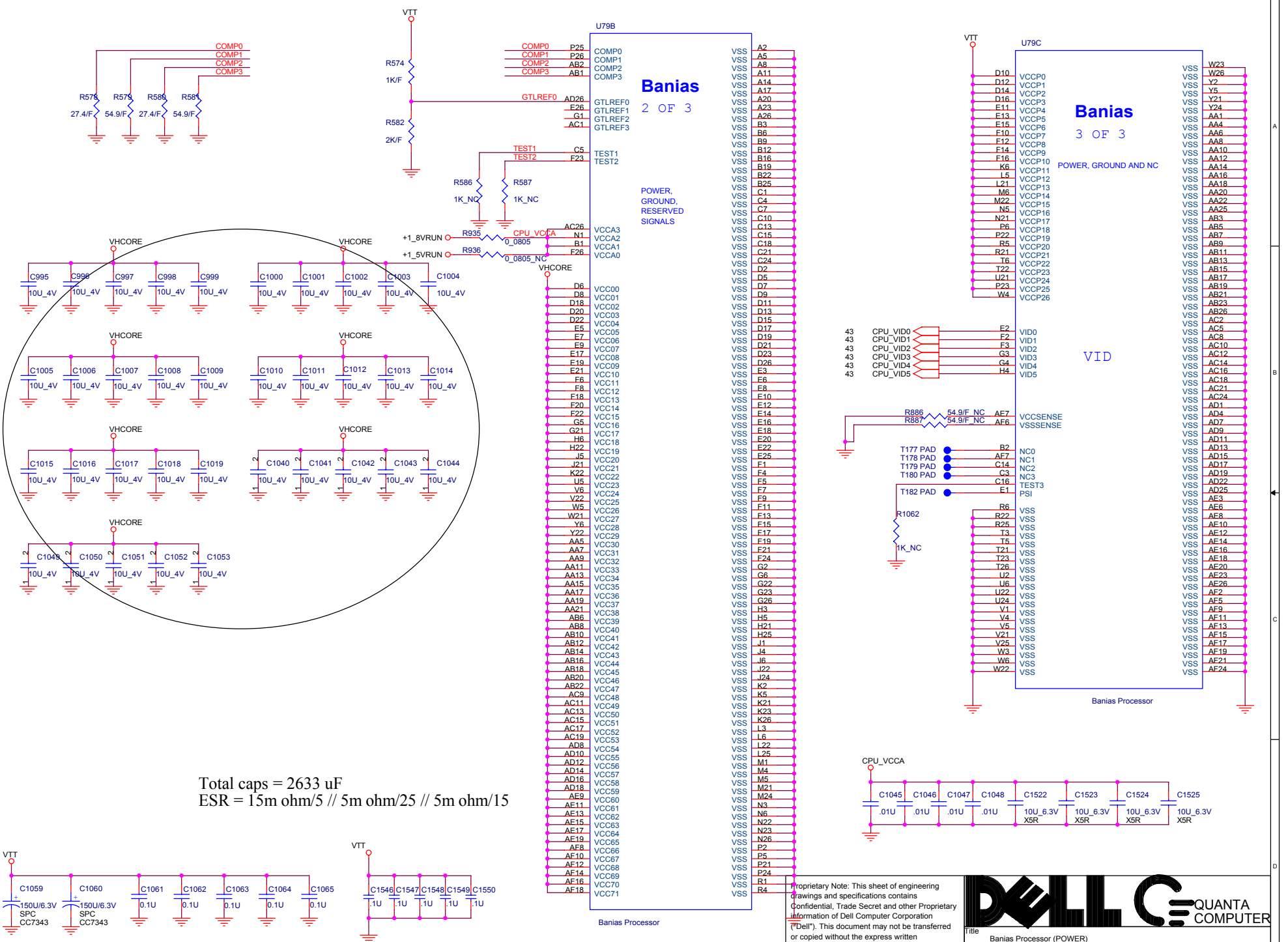
星期五, 六月 06, 2003

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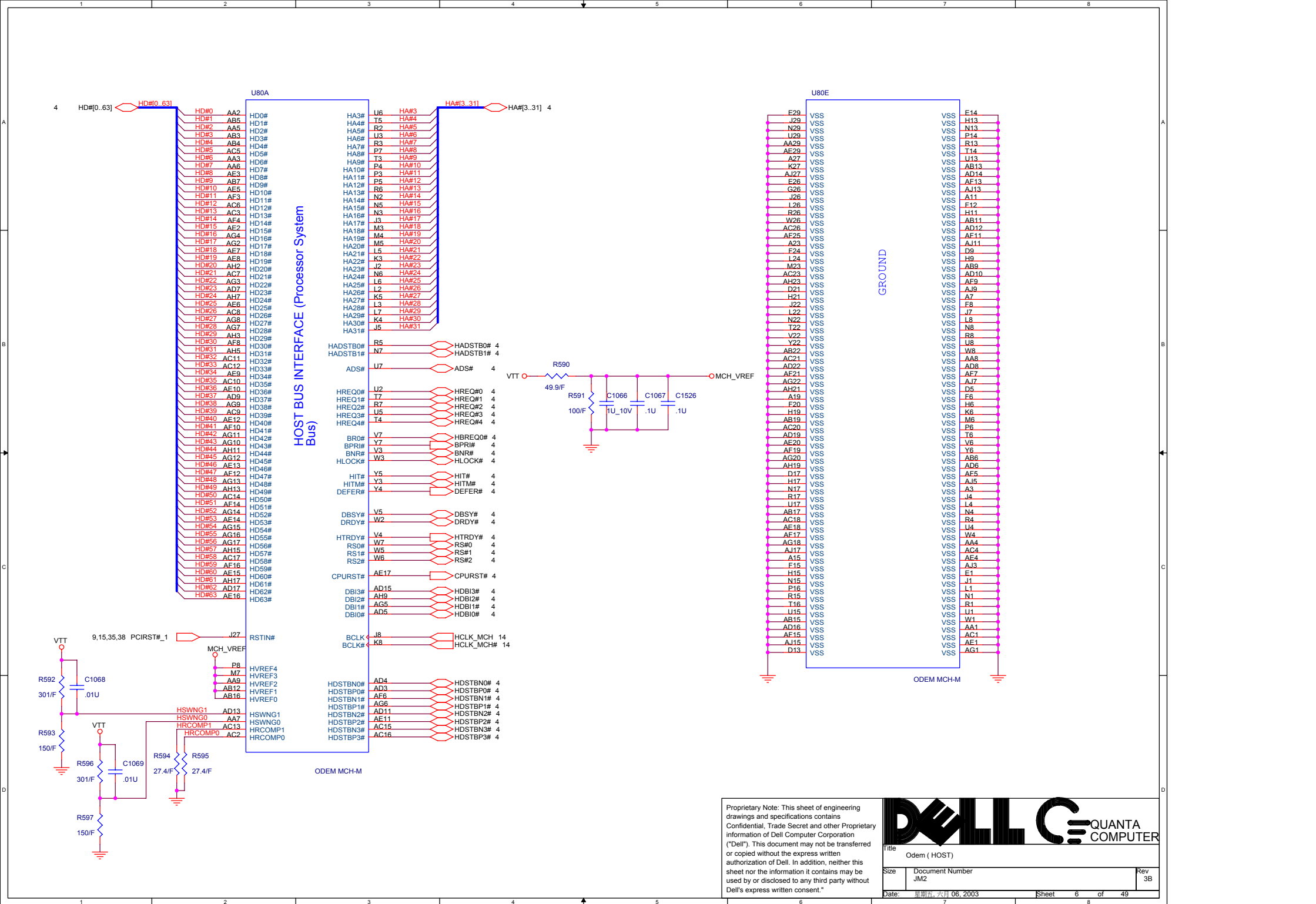


Total caps = 2633 uF
ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15

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Title Banias Processor (POWER)		
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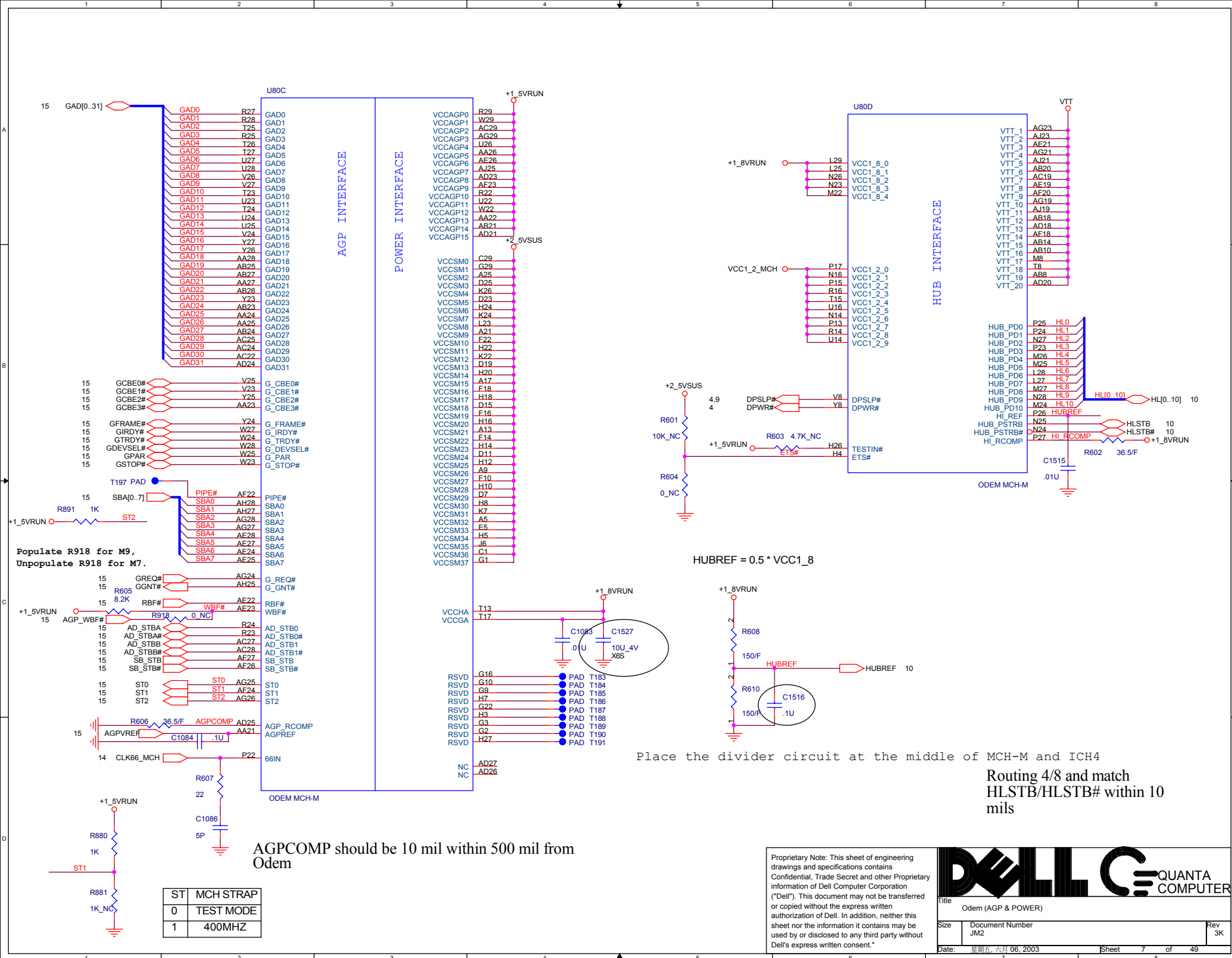


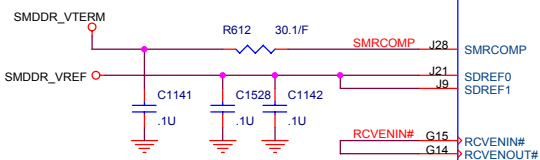
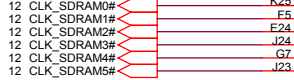
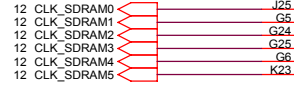
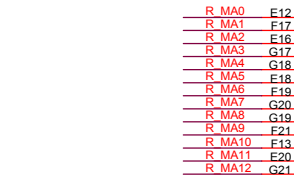
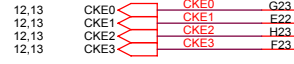
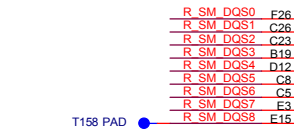
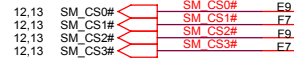
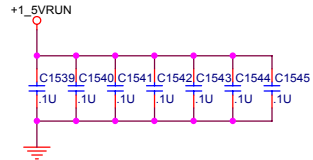
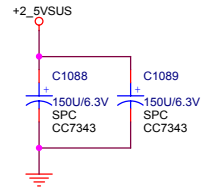
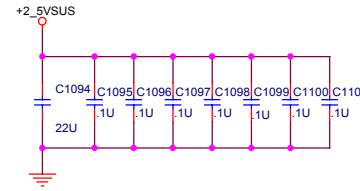
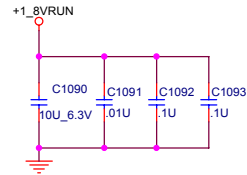
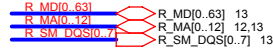
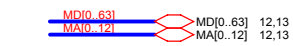
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Size	Document Number
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Date: 星期五, 六月 06, 2003

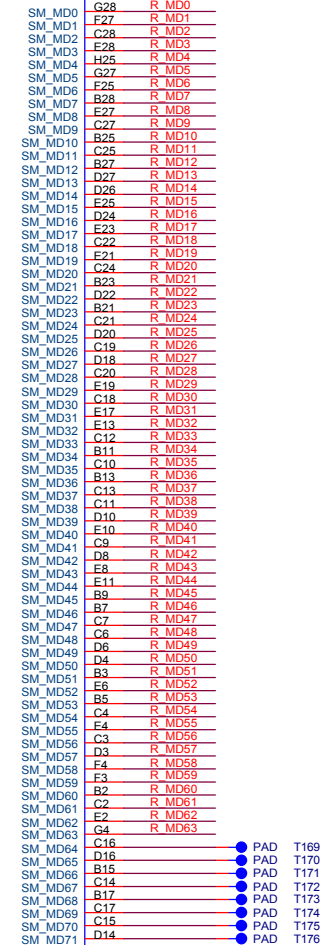
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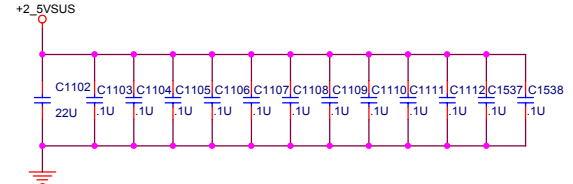


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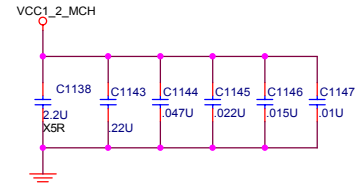
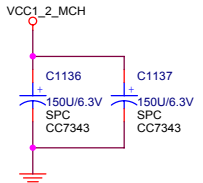
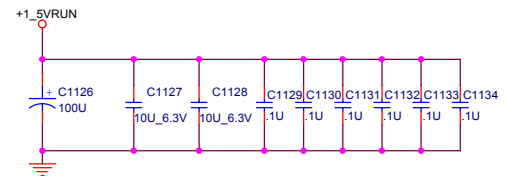
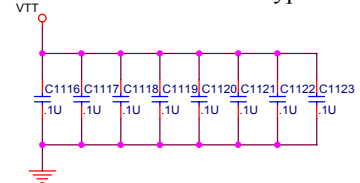
DDR INTERFACE



ODEM MCH-M



Place the bypass caps close to Odem MCH-M

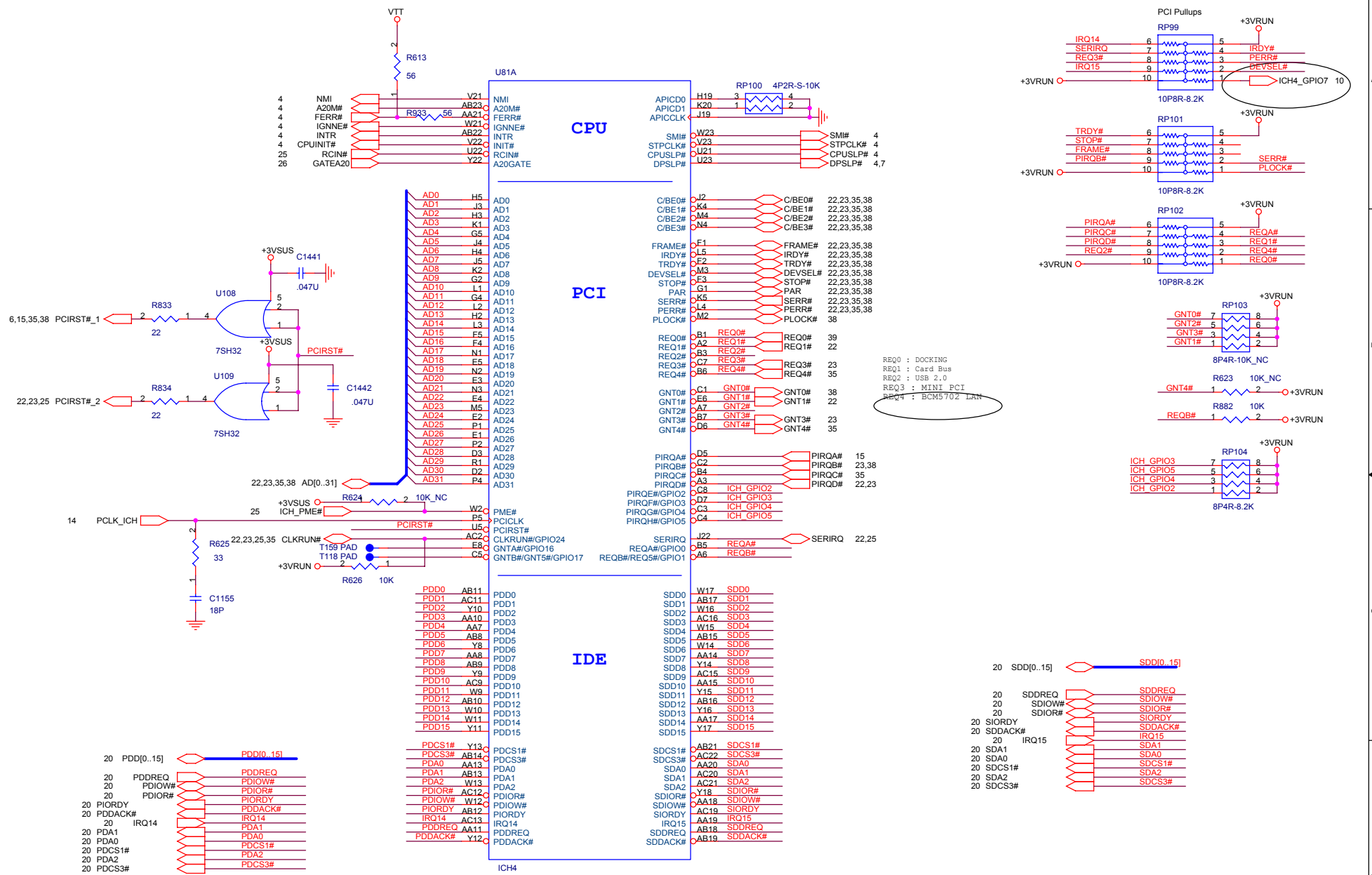


RCVENIN# and RCVENOUT#
ROUTING RULE
L1 - MCH-M Feedback Signal Pin to Signal Via: Max = 40 mils
L2 - MCH-M RCVENOUT# Signal Via to RCVENIN# Signal Via: Must = 100 mils +/- 5mils
L3 - Signal Via to MCH-M Feedback Signal Pin: Max = 40 mils

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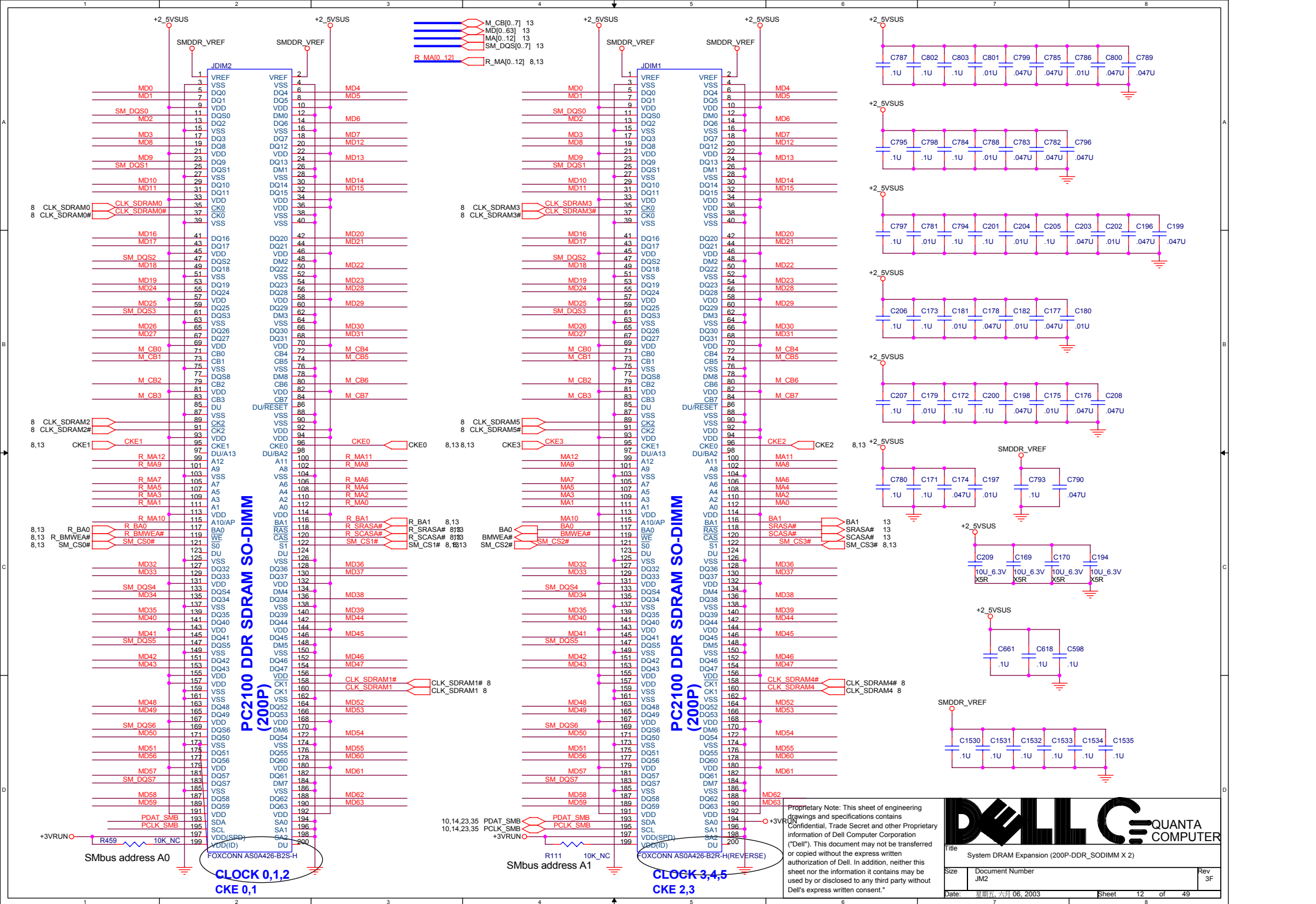


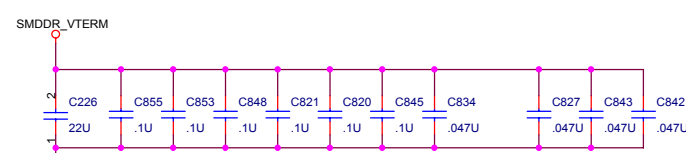
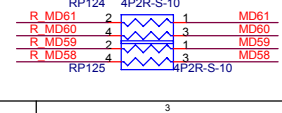
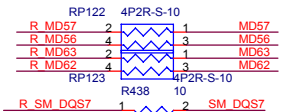
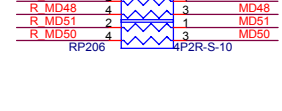
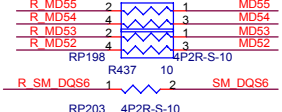
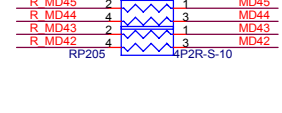
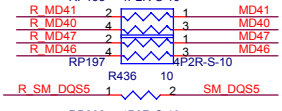
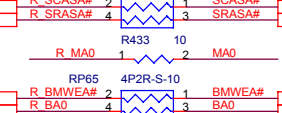
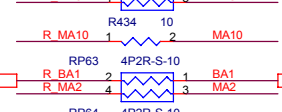
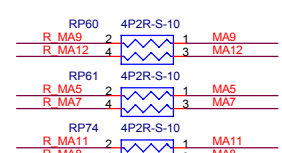
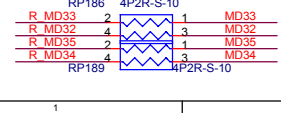
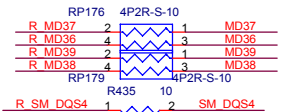
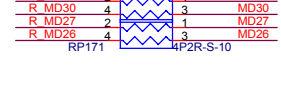
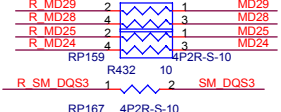
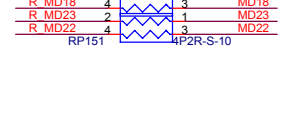
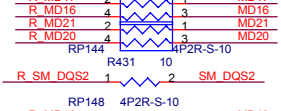
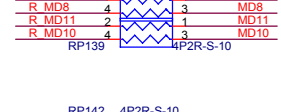
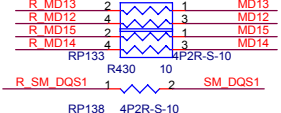
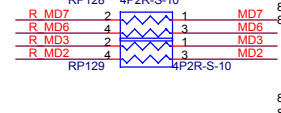
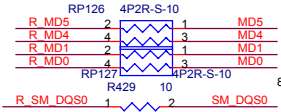
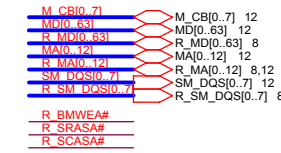
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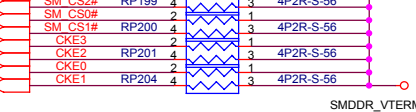
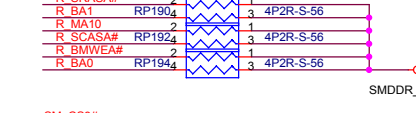
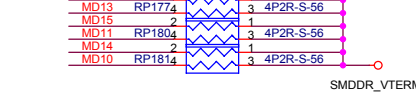
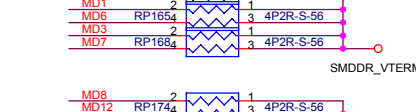
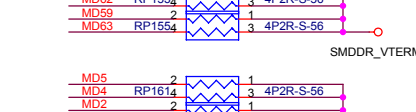
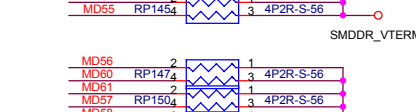
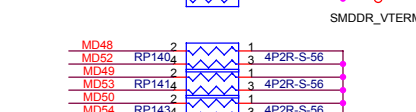
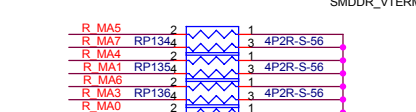
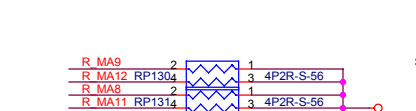
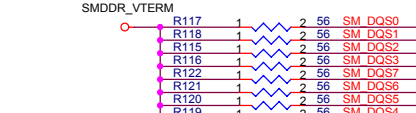
QUANTA
COMPUTER

Title ICH4-M (CPU,PCI,IDE)		
Size	Document Number JM2	Rev 3C
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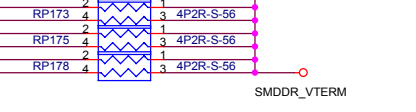
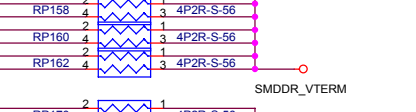
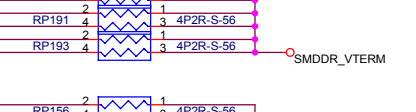
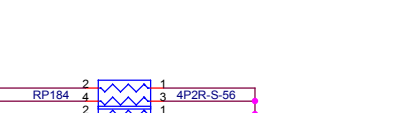
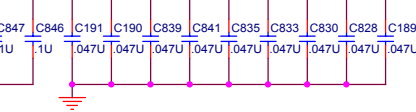
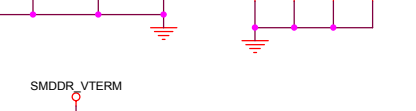
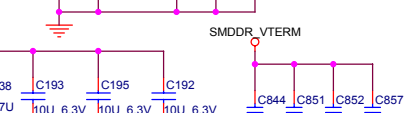
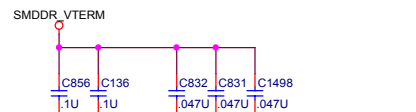




Place Rterms close to JDIMM 1



For terminal R-pack.

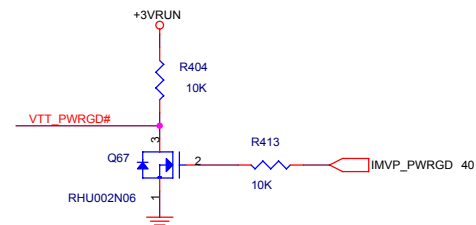
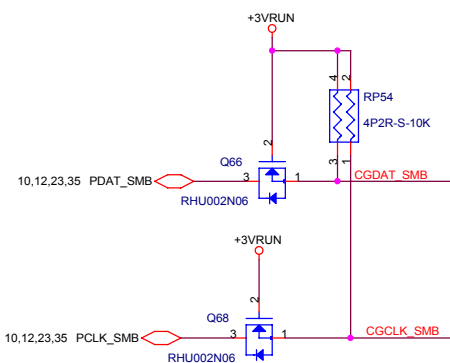
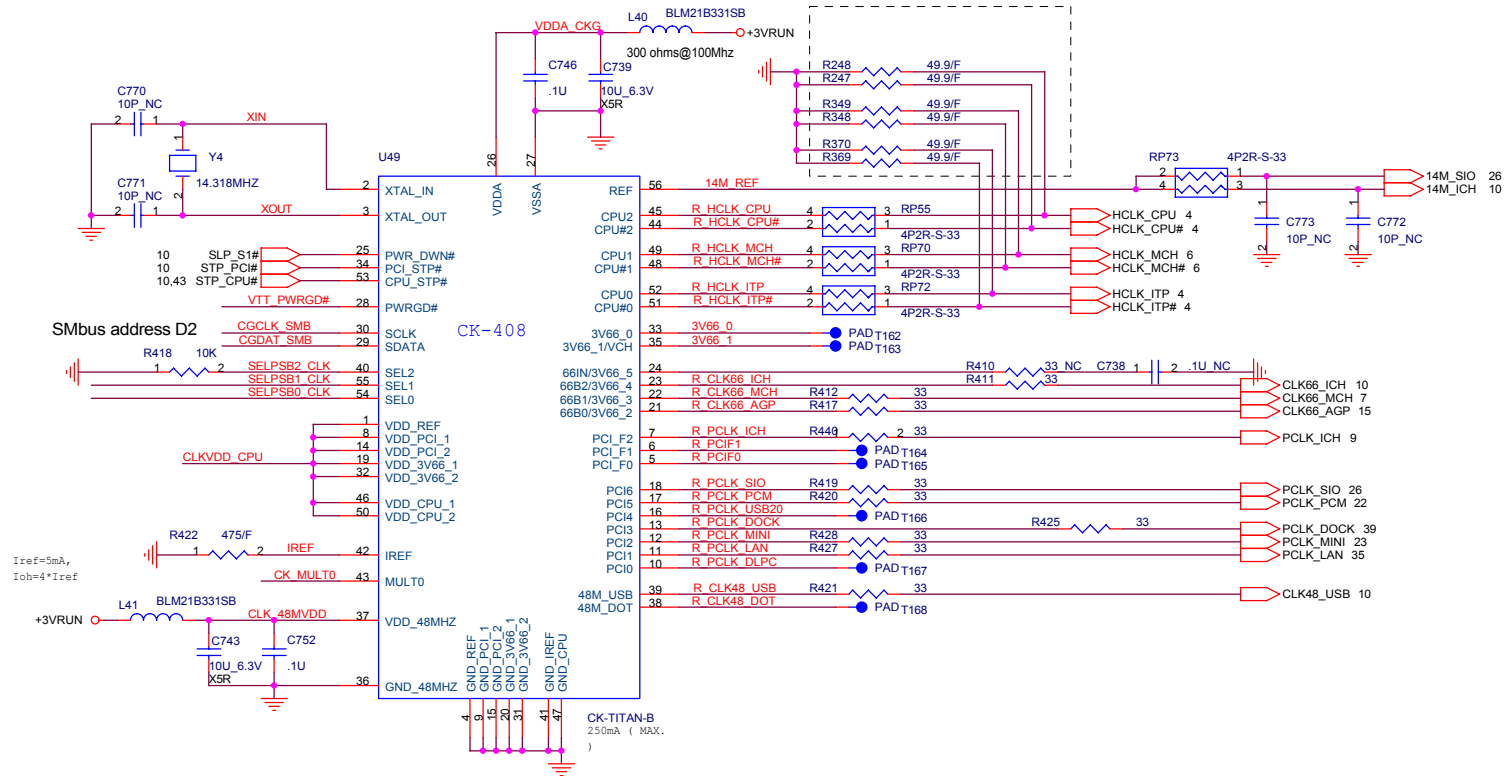
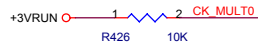
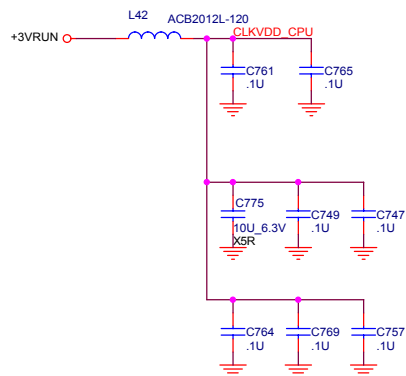
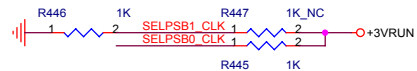


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DELL C QUANTA COMPUTER

File	DDR RES.ARRAY		Rev	1A
Size	Document Number			
	JM2			
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S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

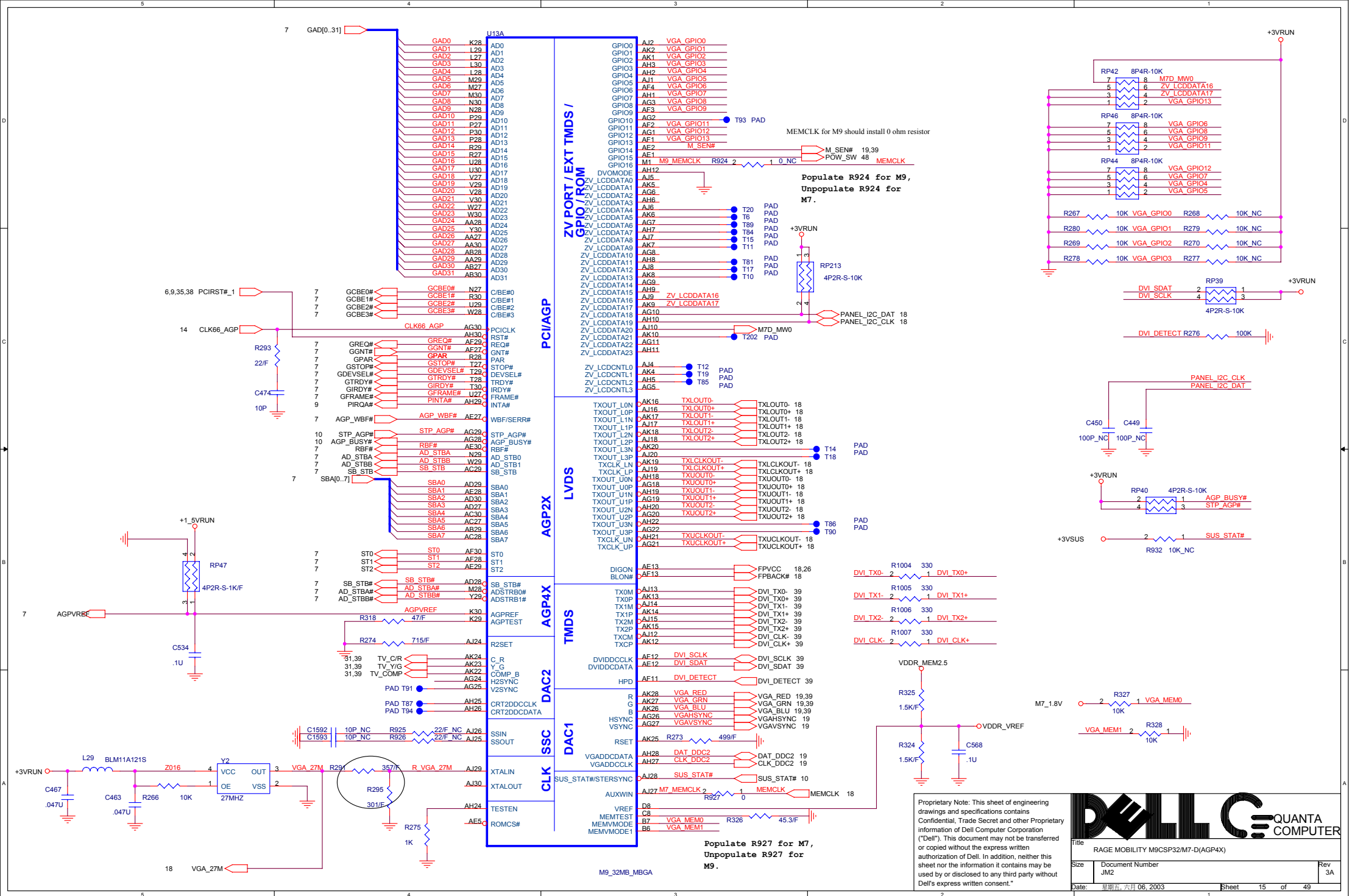


These are for backdrive issue

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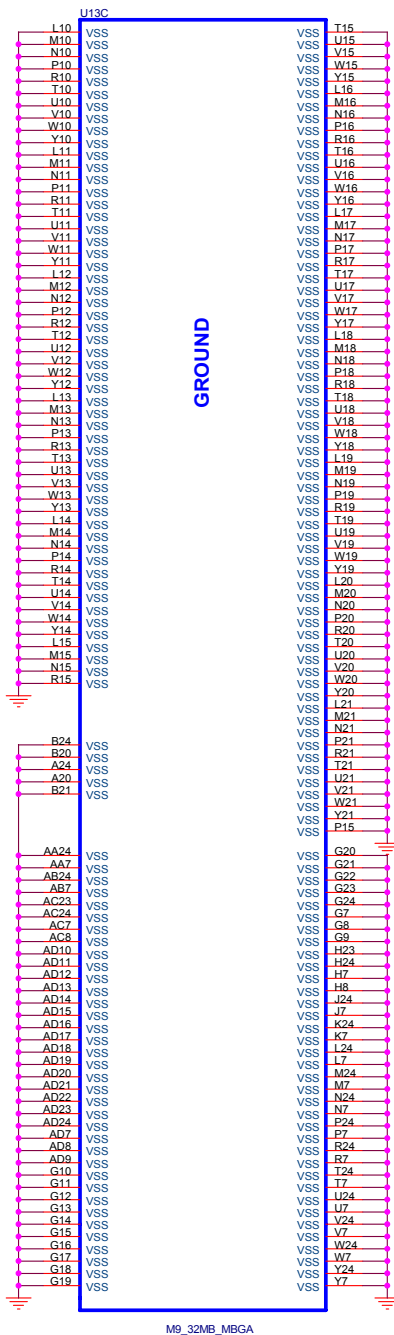


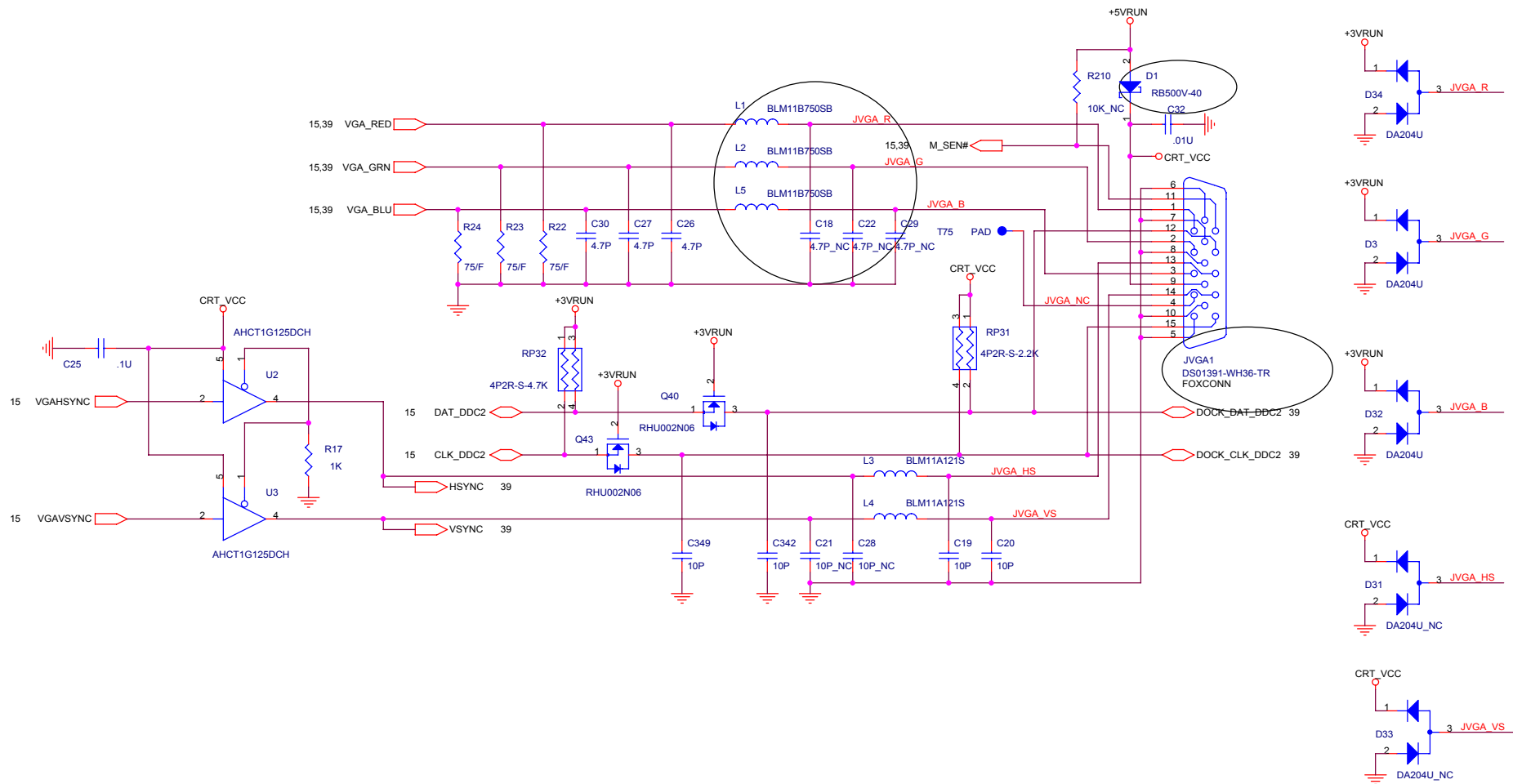
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CLOCK GENERATOR		
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U13B		
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J29	MDQA1	MDQB1 D7
H30	MDQA2	MDQB2 D6
H29	MDQA3	MDQB3 C4
F30	MDQA4	MDQB4 D4
F29	MDQA5	MDQB5 C3
E30	MDQA6	MDQB6 D3
E29	MDQA7	MDQB7 A5
J28	MDQA8	MDQB8 B5
J27	MDQA9	MDQB9 A4
H28	MDQA10	MDQB10 B4
H27	MDQA11	MDQB11 A2
F28	MDQA12	MDQB12 B2
F27	MDQA13	MDQB13 A1
E28	MDQA14	MDQB14 B1
E27	MDQA15	MDQB15 E3
D30	MDQA16	MDQB16 F3
D29	MDQA17	MDQB17 F4
C30	MDQA18	MDQB18 H4
C29	MDQA19	MDQB19 H3
A30	MDQA20	MDQB20 J4
A29	MDQA21	MDQB21 J3
A28	MDQA22	MDQB22 J4
B28	MDQA23	MDQB23 C1
D28	MDQA24	MDQB24 D1
C28	MDQA25	MDQB25 D2
D27	MDQA26	MDQB26 F1
C27	MDQA27	MDQB27 F2
D26	MDQA28	MDQB28 G1
C26	MDQA29	MDQB29 G2
D24	MDQA30	MDQB30 R1
C24	MDQA31	MDQB31 R2
D18	MDQA32	MDQB32 T1
C18	MDQA33	MDQB33 T2
D17	MDQA34	MDQB34 V1
C17	MDQA35	MDQB35 V2
D15	MDQA36	MDQB36 W1
C15	MDQA37	MDQB37 W2
D14	MDQA38	MDQB38 T3
C14	MDQA39	MDQB39 T4
B17	MDQA40	MDQB40 U3
A17	MDQA41	MDQB41 U4
B16	MDQA42	MDQB42 W3
A16	MDQA43	MDQB43 W4
B14	MDQA44	MDQB44 Y4
A14	MDQA45	MDQB45 Y1
B13	MDQA46	MDQB46 Y2
A13	MDQA47	MDQB47 AA1
D13	MDQA48	MDQB48 AA2
C13	MDQA49	MDQB49 AC1
D12	MDQA50	MDQB50 AC2
C12	MDQA51	MDQB51 AD1
D10	MDQA52	MDQB52 AD2
C10	MDQA53	MDQB53 AA3
D9	MDQA54	MDQB54 AA4
C9	MDQA55	MDQB55 AB3
B12	MDQA56	MDQB56 AB4
A12	MDQA57	MDQB57 AD3
B11	MDQA58	MDQB58 AD4
A11	MDQA59	MDQB59 AE3
B9	MDQA60	MDQB60 AE4
A9	MDQA61	
B8	MDQA62	
A8	MDQA63	
B26	MAA0	MAB0 H2
A26	MAA1	MAB1 H1
B25	MAA2	MAB2 J2
A25	MAA3	MAB3 J1
C22	MAA4	MAB4 K4
D21	MAA5	MAB5 K3
C21	MAA6	MAB6 L4
D20	MAA7	MAB7 M3
C20	MAA8	MAB8 L3
D22	MAA9	MAB9 M4
C23	MAA10	MAB10 N2
D23	MAA11	MAB11 N1
A27	MAA12	MAB12 N4
B27	MAA13	MAB13 N3
G30C	QMA#0	DQMB#0 D5
G28C	QMA#1	DQMB#1 B3
B30C	QMA#2	DQMB#2 G3
D26C	QMA#3	DQMB#3 E1
D16C	QMA#4	DQMB#4 U1
B15C	QMA#5	DQMB#5 V3
D11C	QMA#6	DQMB#6 AB1
B10C	QMA#7	DQMB#7 AC3
G29	QSA0	QSB0 C5
G27	QSA1	QSB1 A3
B28	QSA2	QSB2 G4
C26	QSA3	QSB3 E2
C16	QSA4	QSB4 U2
A15	QSA5	QSB5 V4
C11	QSA6	QSB6 AB2
A10	QSA7	QSB7 AC4
B18C	RASA#	RASB# P4
A19C	CASA#	CASB# R3
C18C	WEA#	WEB# P2
B19C	CSA#0	CSB#0 P3
D19	CSA#1	CSB#1 P1
B23	CKEA	CKEB P1
A23	CLKA0	CLKB0 K2
B22C	CLKA0#	CLKB0# K1
A22	CLKA1	CLKB1 L2
A21C	CLKA1#	CLKB1# L1
	CLKAFB	CLKBFB M2

M9_32MB_MBGa

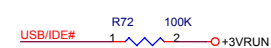
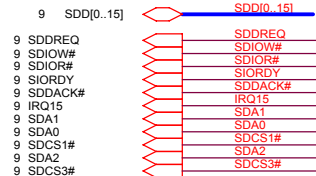
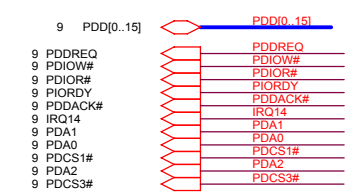
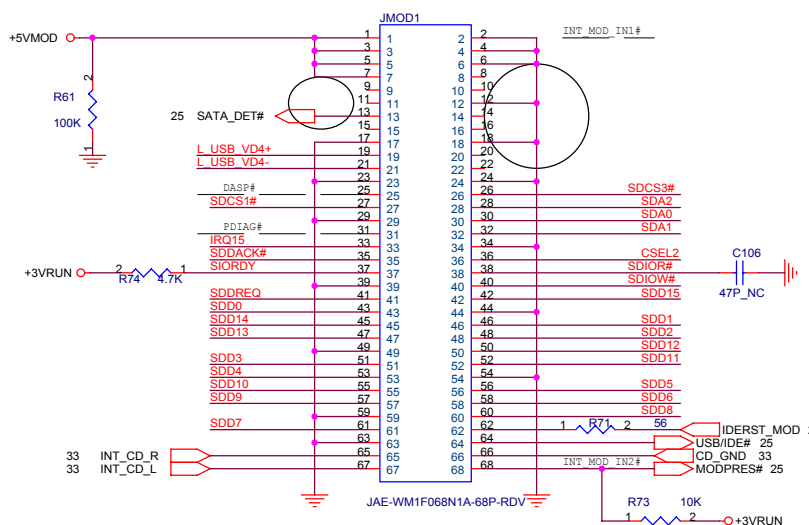
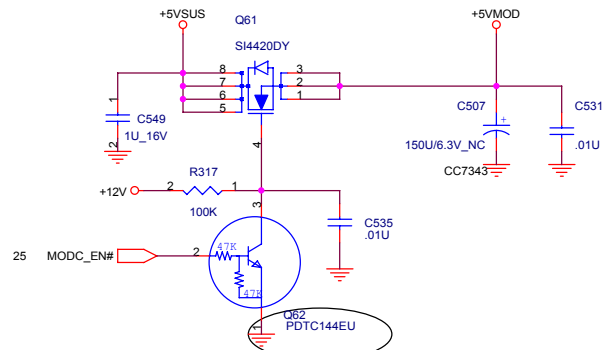
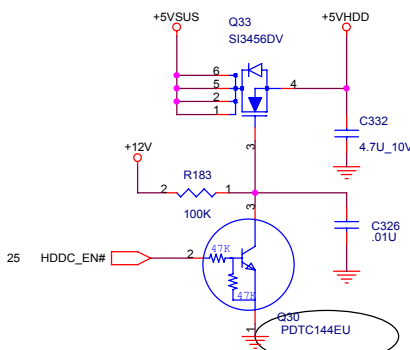
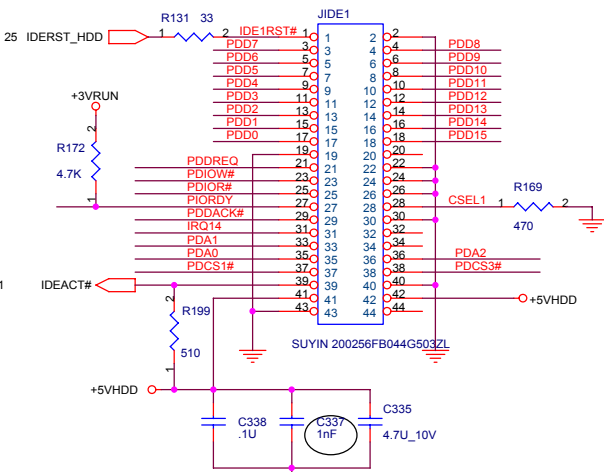
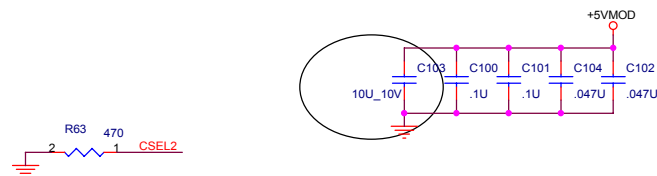
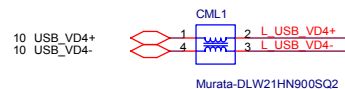
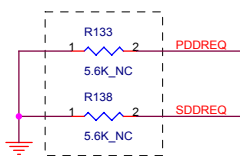




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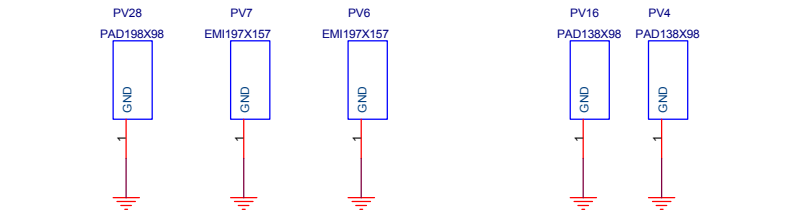
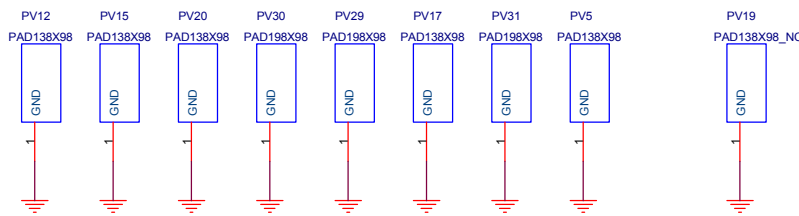
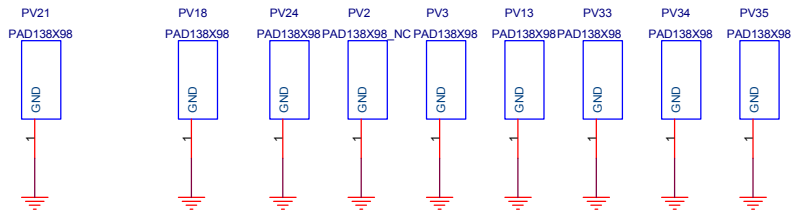
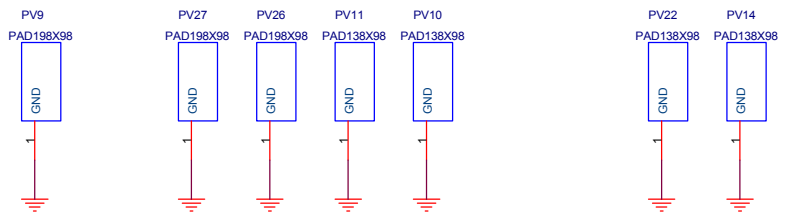
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Size	Document Number	Rev	
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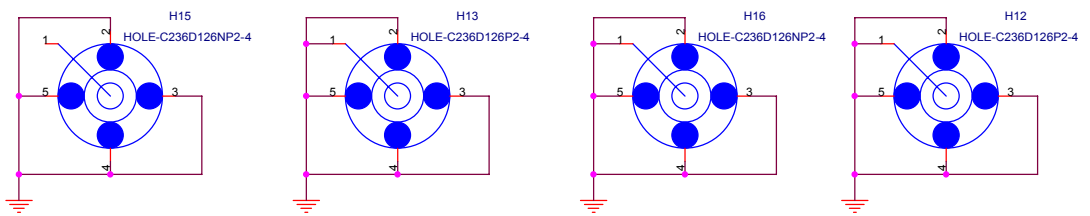
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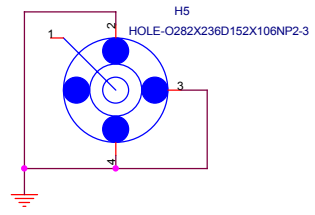
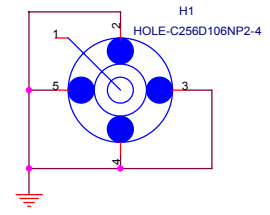
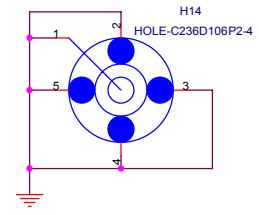
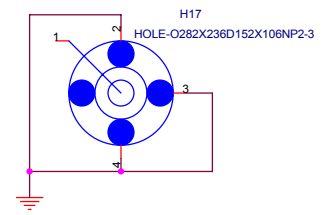
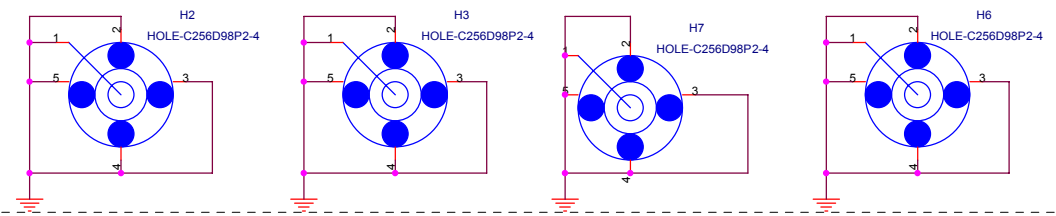
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FOR DOCKING

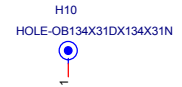
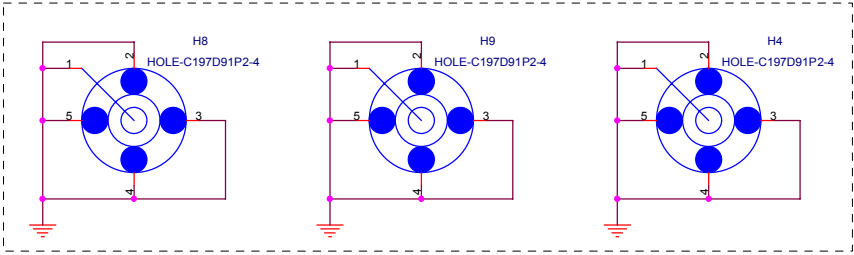
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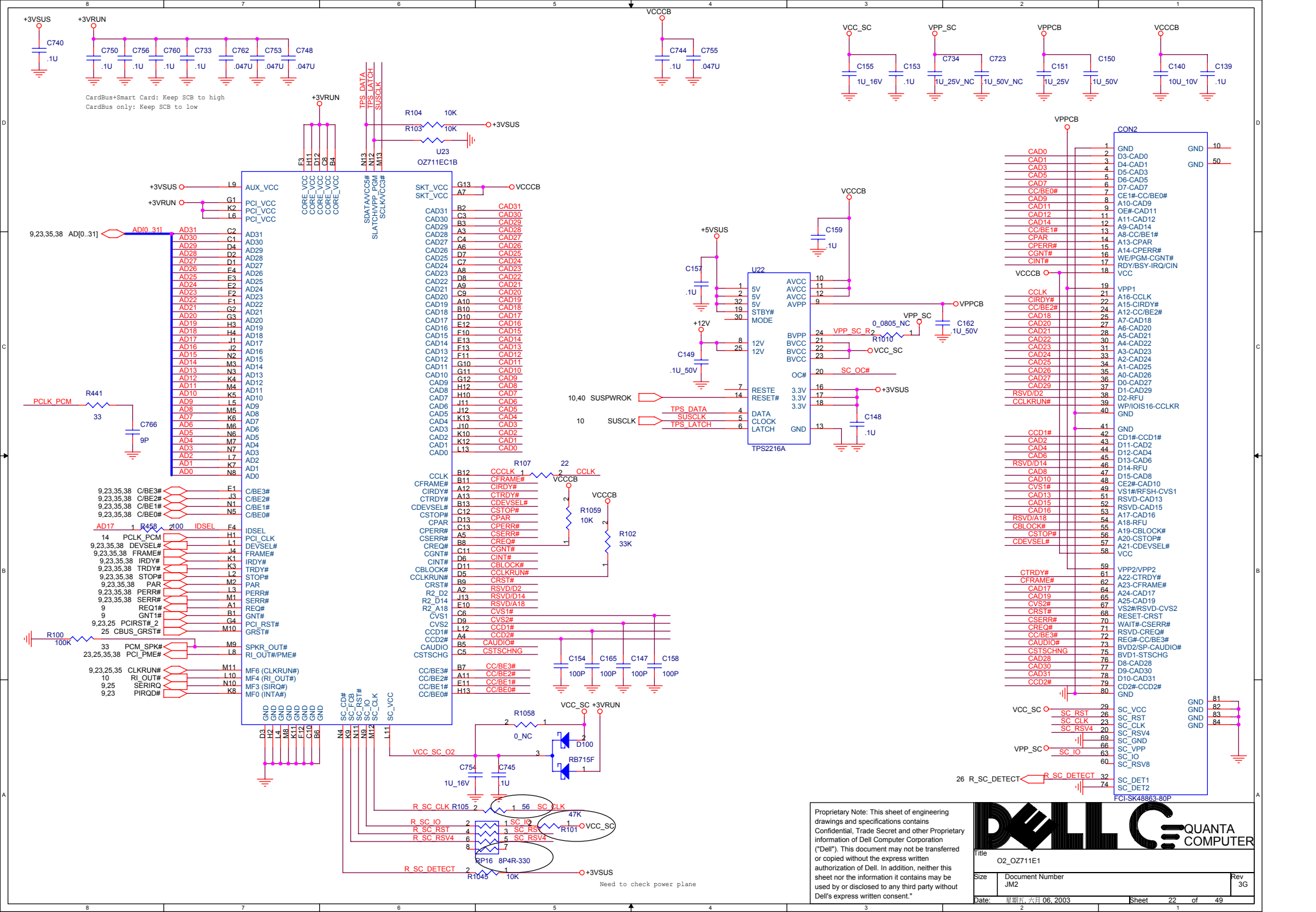
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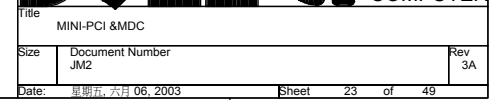
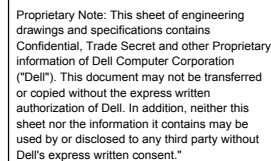


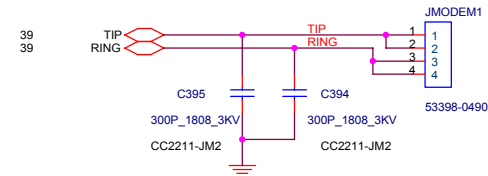
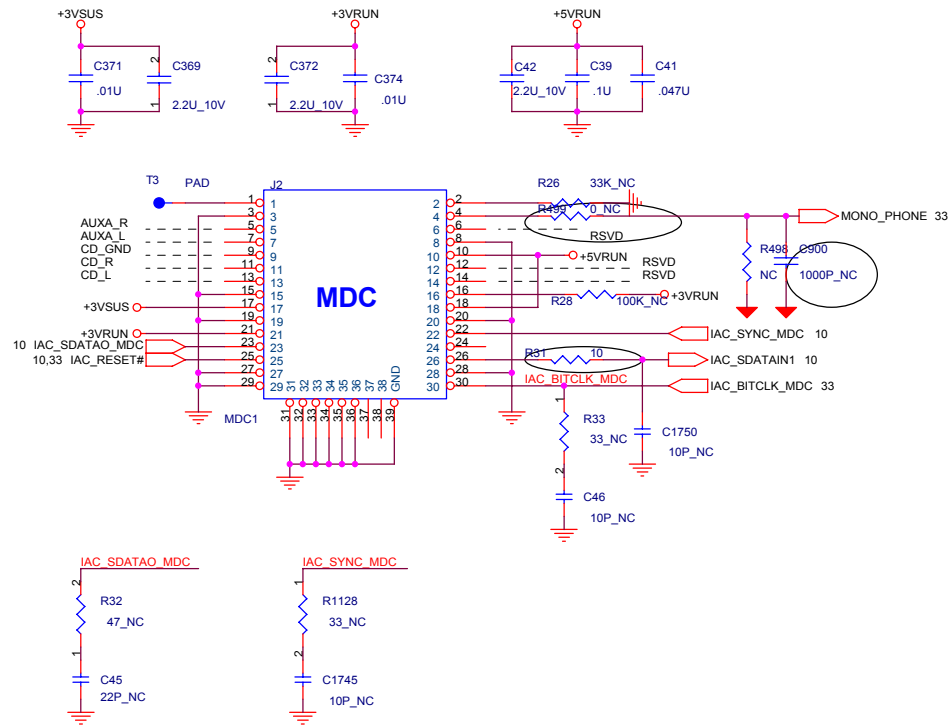
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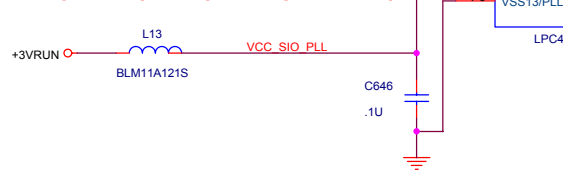
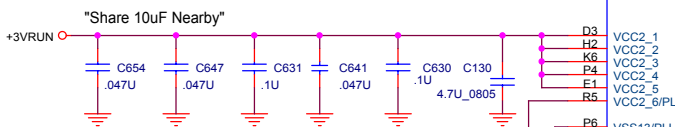
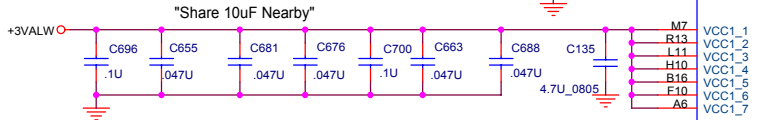
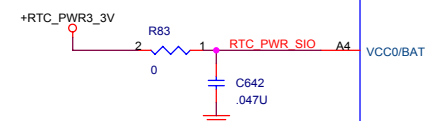
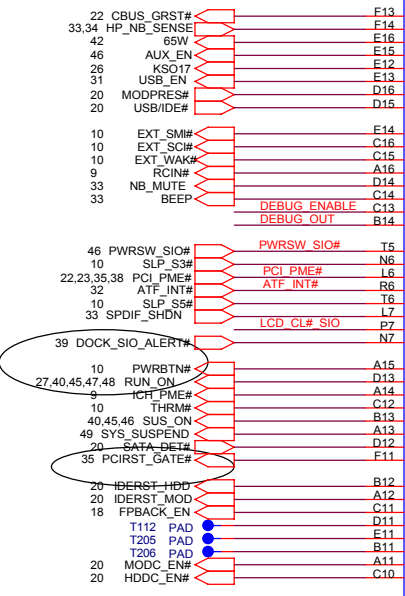
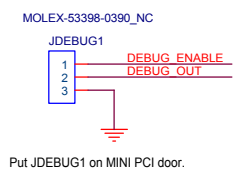
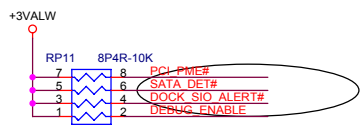
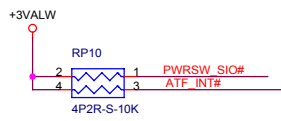




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LPC47N254
MACALLEN
1 OF 2 DOCK LPC
256 - LBGA

8051
GPIO

LPC

FDD

LPC
GPIO

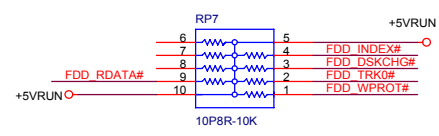
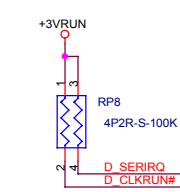
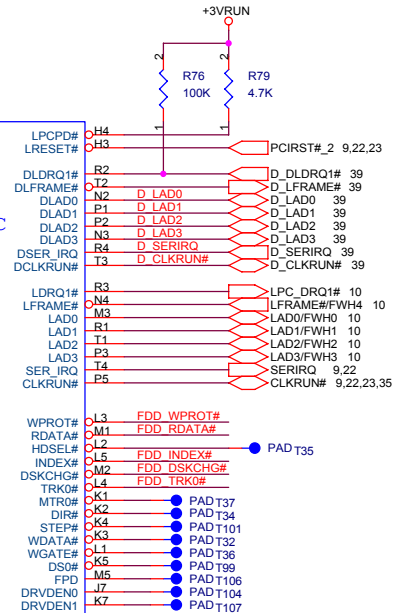
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IR

LPT

VCC

GND



PAD T35

10,27

PD[0..7] 28

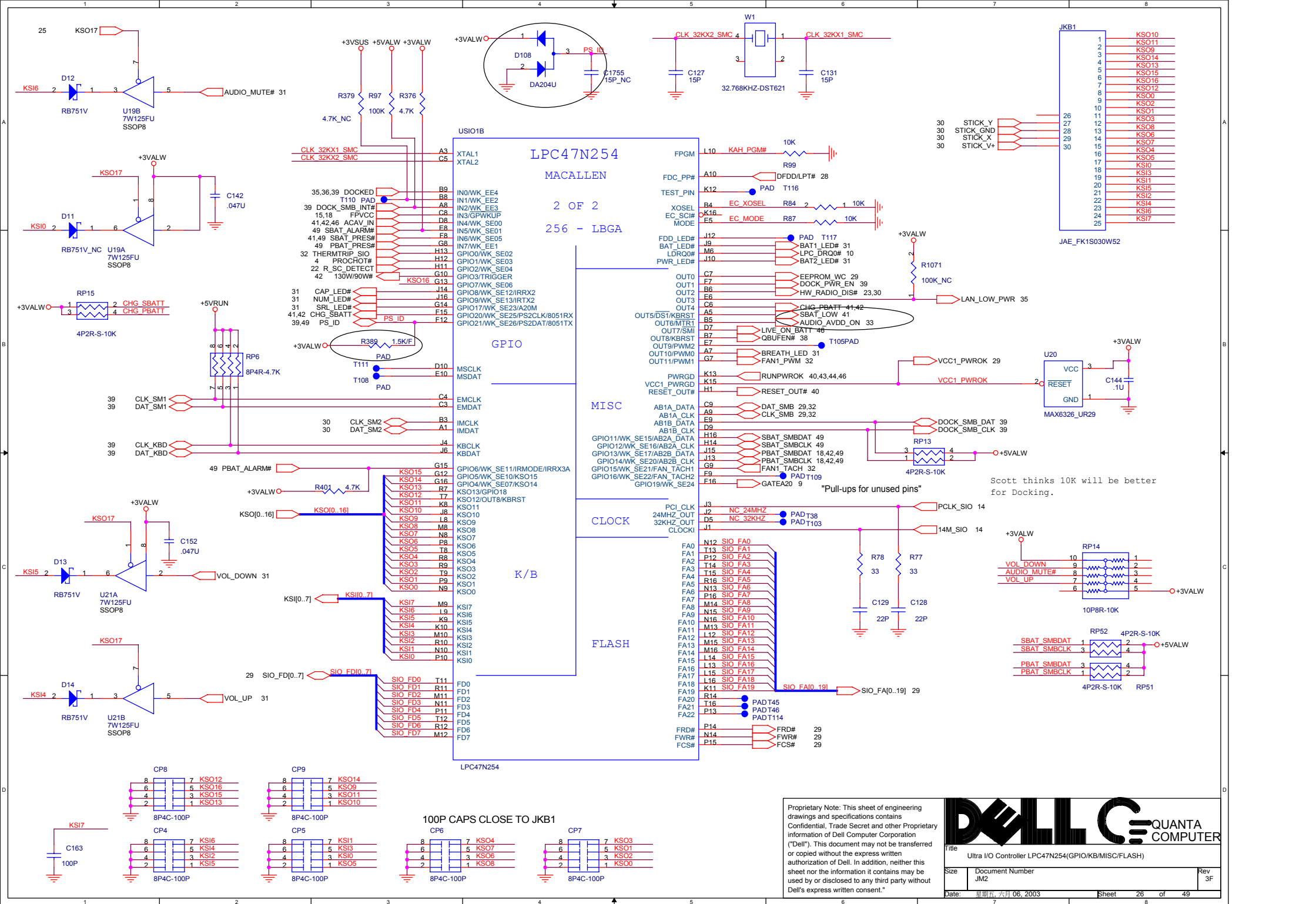
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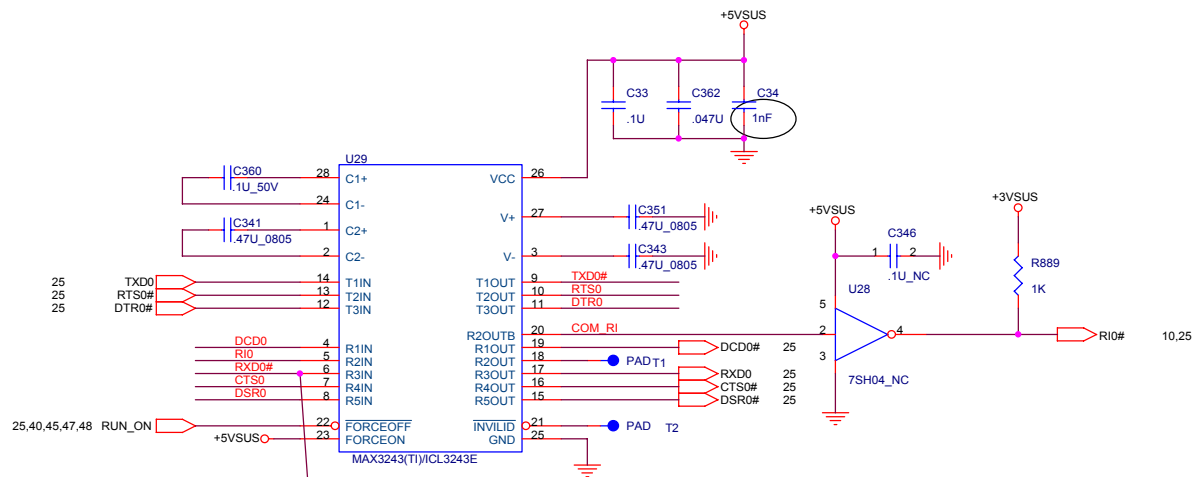
L36

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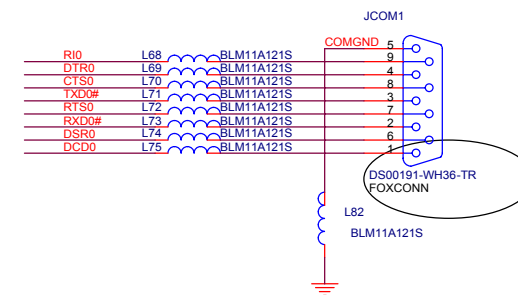


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Size	Document Number	Rev	
	JM2	3D	
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If MAX3243 pin 22 tied to RUN_ON, then it can not support Ring Out

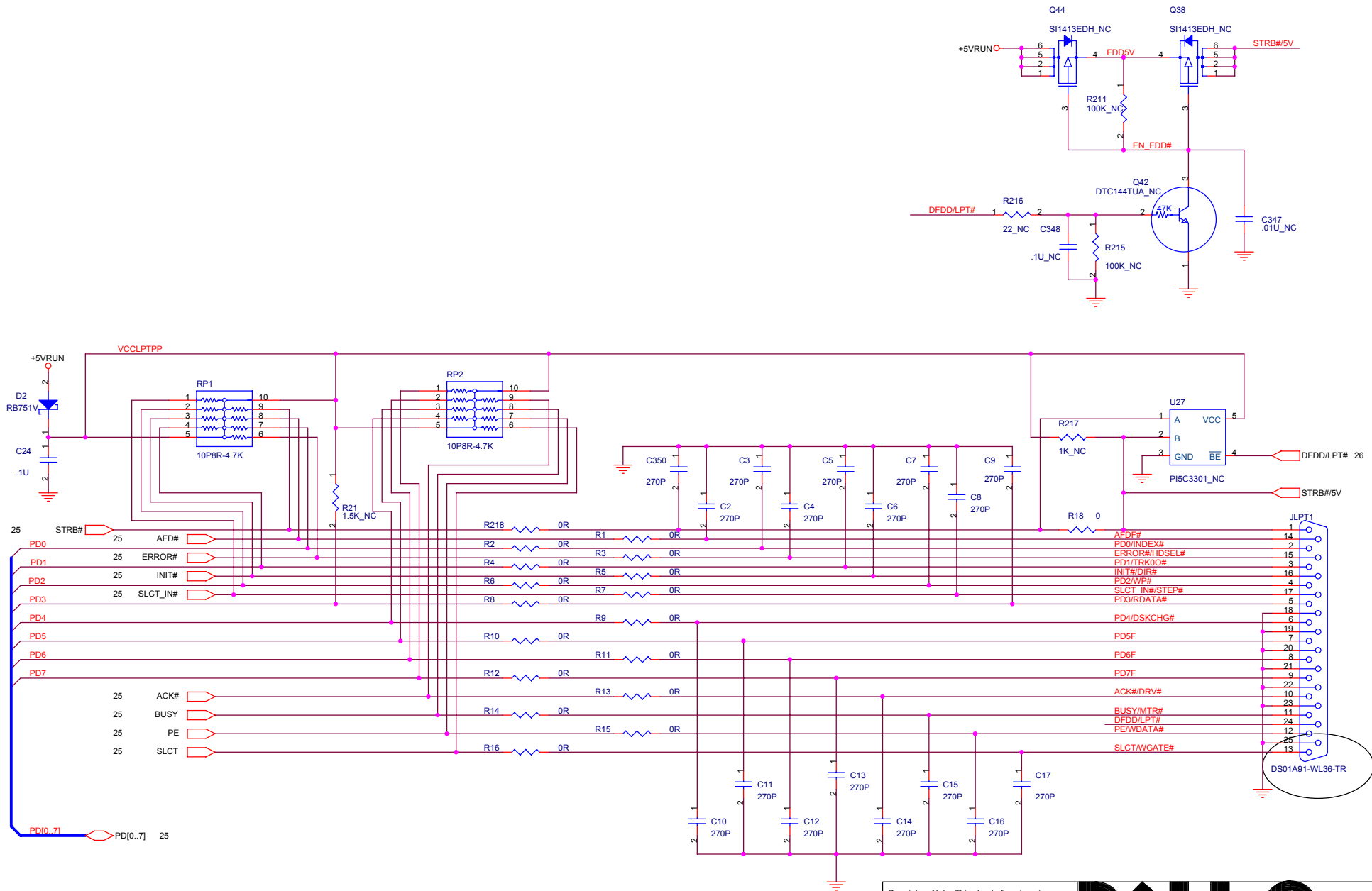


Place these beads close to JCOM1 as soon as possible

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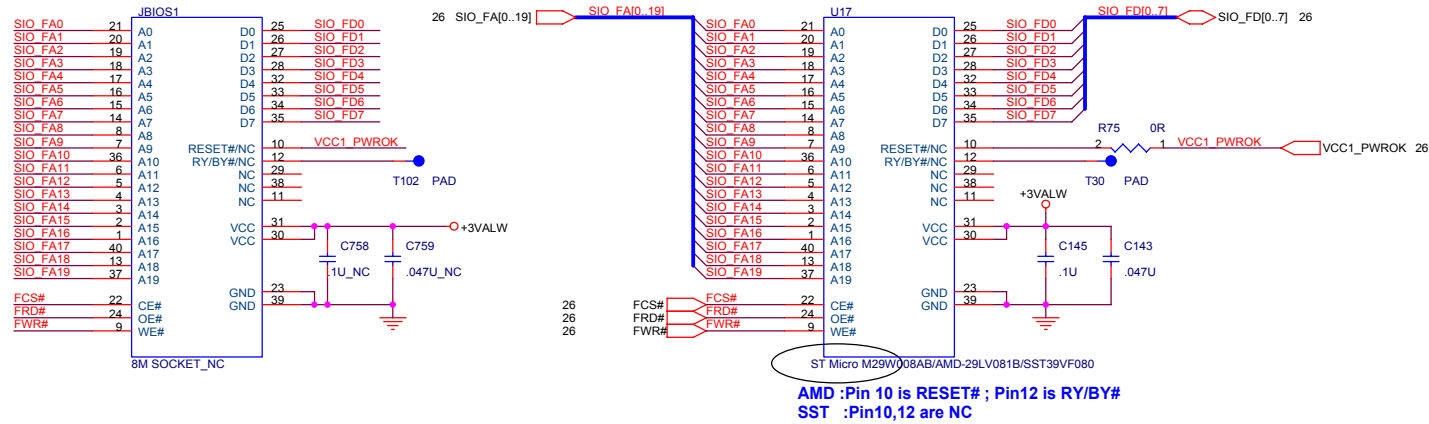
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Size	Document Number	Rev	
	JM2	3K	
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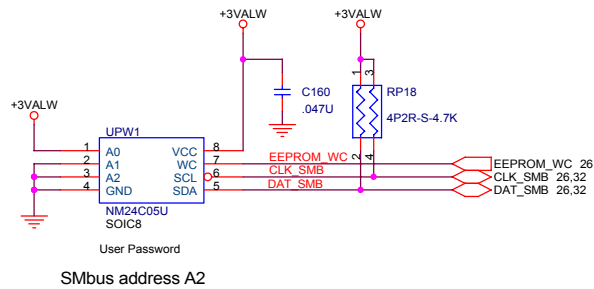
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File PARALLEL CONN.		
Size	Document Number JM2	Rev 3F
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8Mbit (1M Byte),NO PLCC TYPE



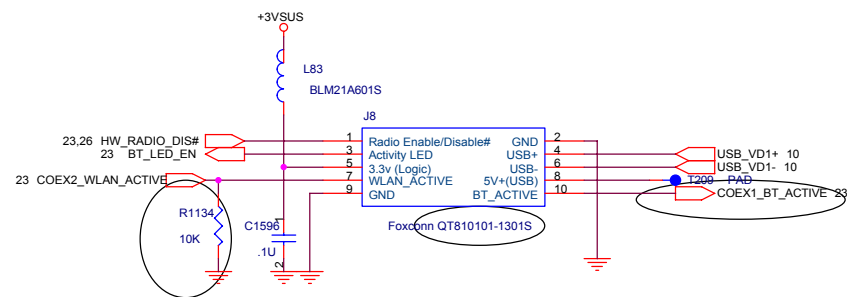
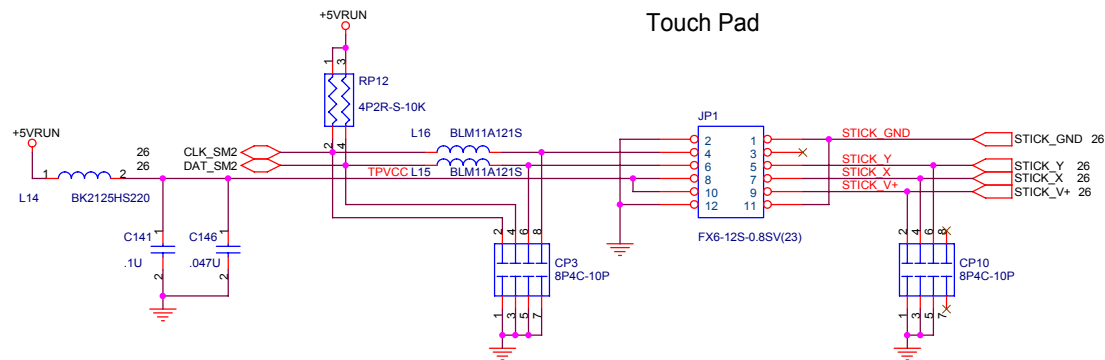
- 1.AMD-29LV081B require MAX 500nS Tready for it's hardware reset.And MAX6326_UR29 has >100mS reset timing.So we can tie it's reset# pin to +3VALW directly.
- 2.SIO has internal 20 mS delay of VCC1_PWROK



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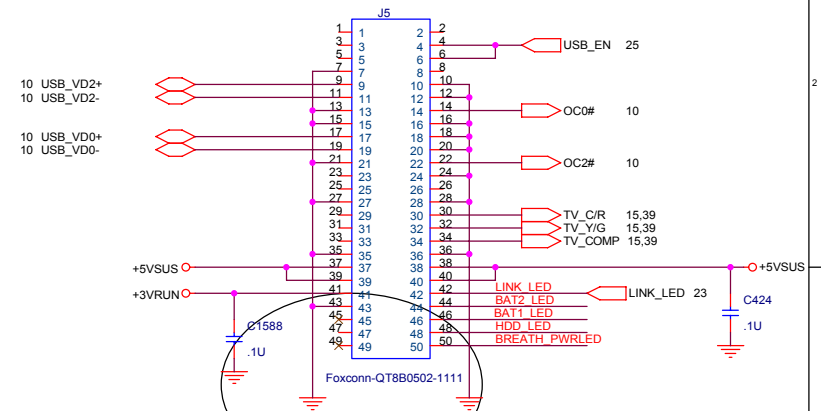
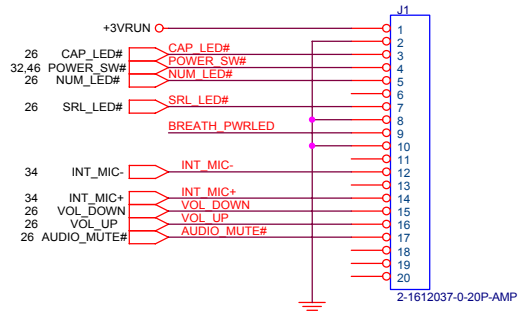
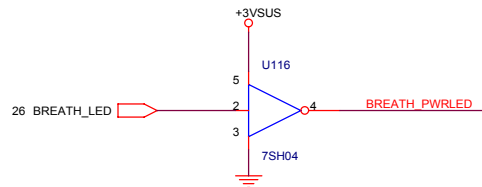
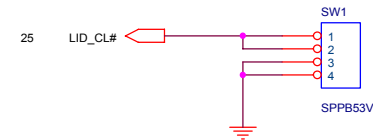
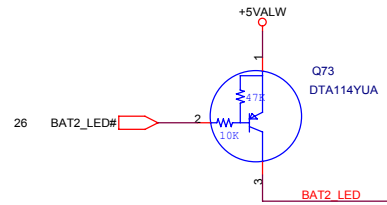
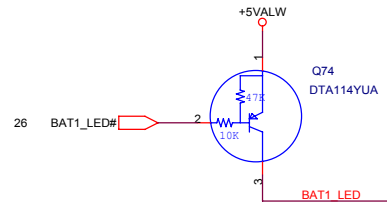
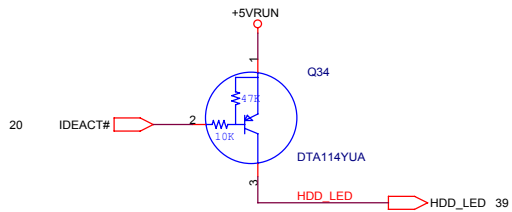
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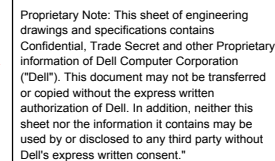
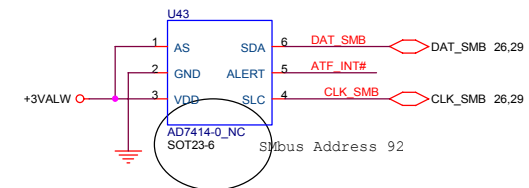
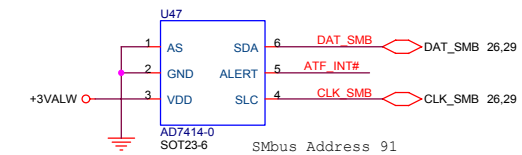
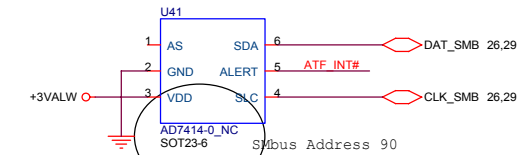
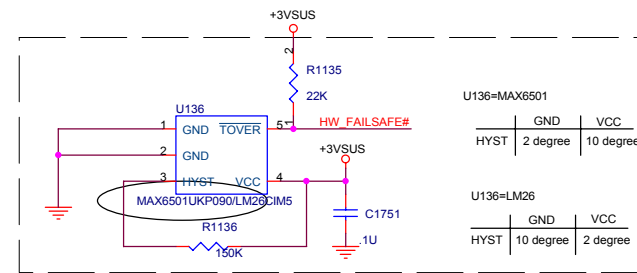
File	TOUCH PAD & BULE TOOTH		
Size	Document Number	Rev	
	JM2	3A	
Date:	星期五, 六月 06, 2003	Sheet	30 of 49



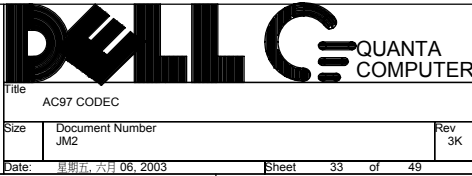
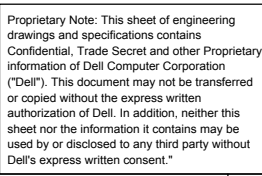
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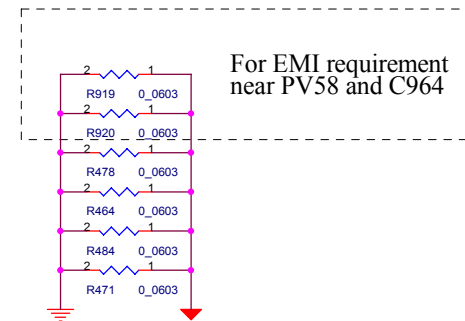
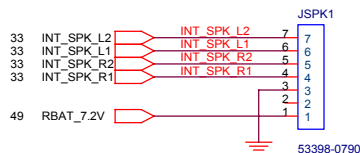
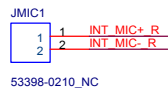
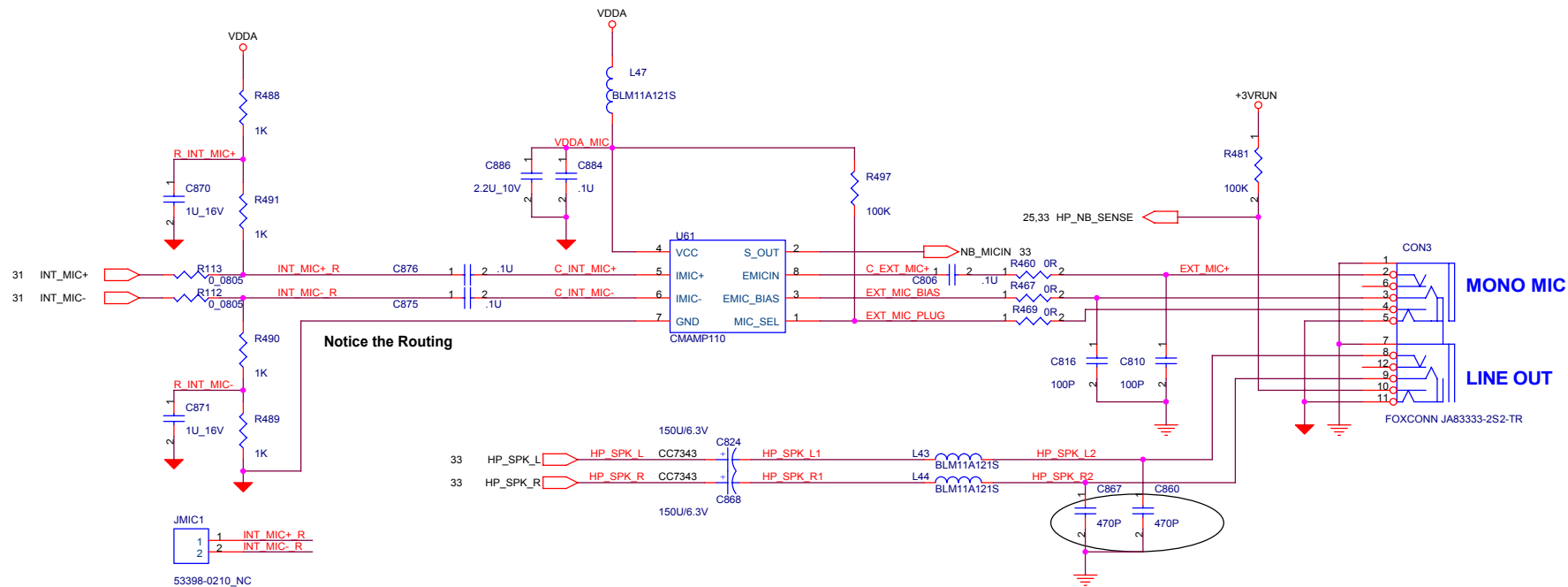


File		SWITCH & LED	
Size	Document Number	Rev	
	JM2	3G	
Date:	星期五, 六月 06, 2003	Sheet	31 of 49



Title			
FAN & THERMAL			
Size	Document Number		Rev
	JM2		3K
Date:	星期五, 六月 06, 2003	Sheet	32 of 49

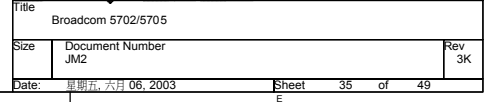
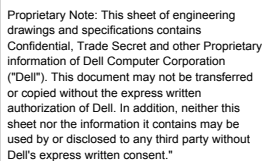


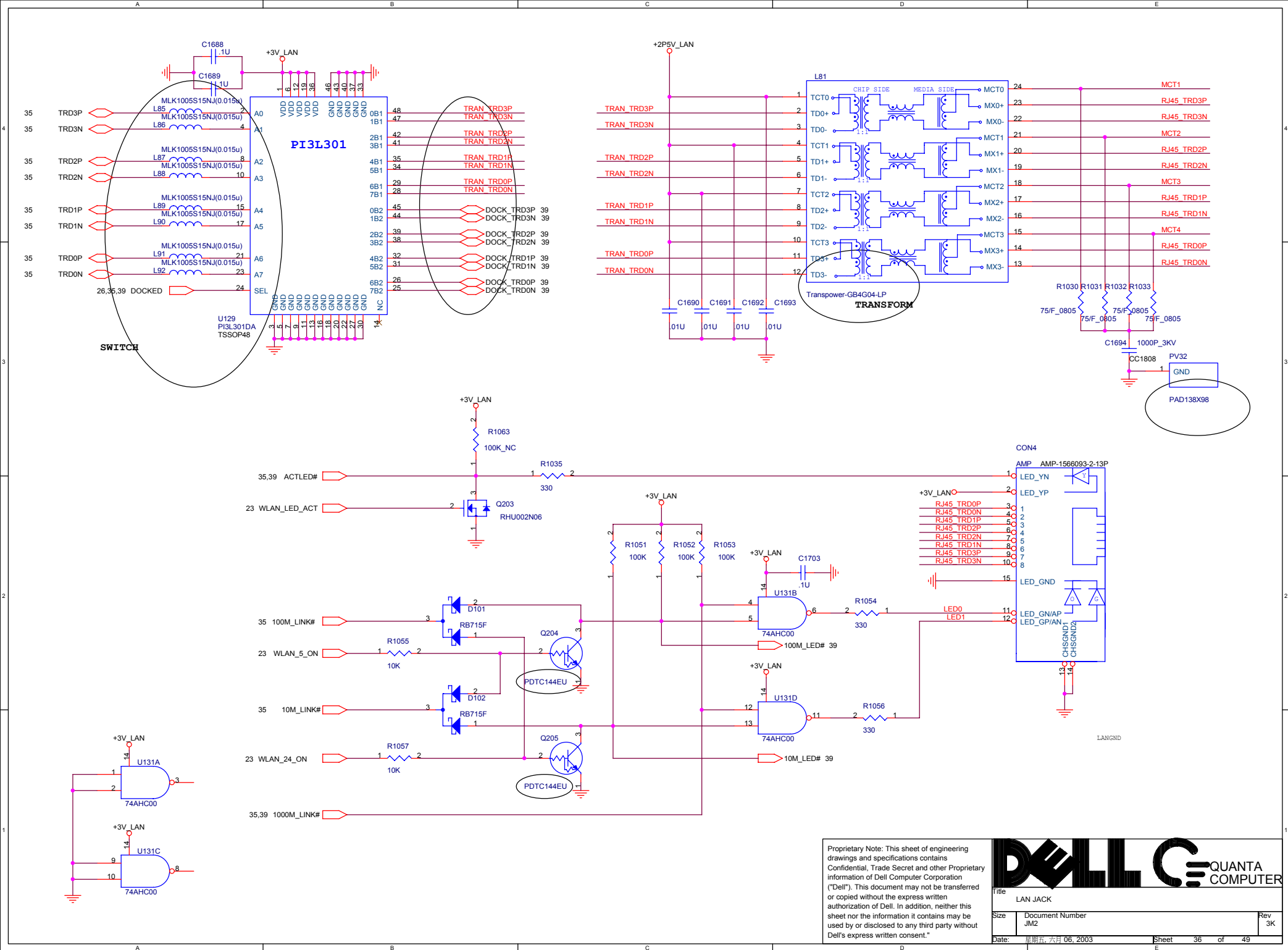


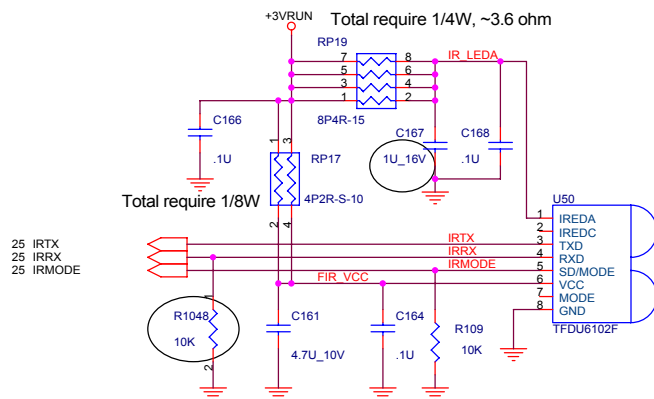
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File	AUDIO CONN		
Size	Document Number	Rev	
	JM2	3F	
Date:	星期五, 六月 06, 2003	Sheet	34 of 49





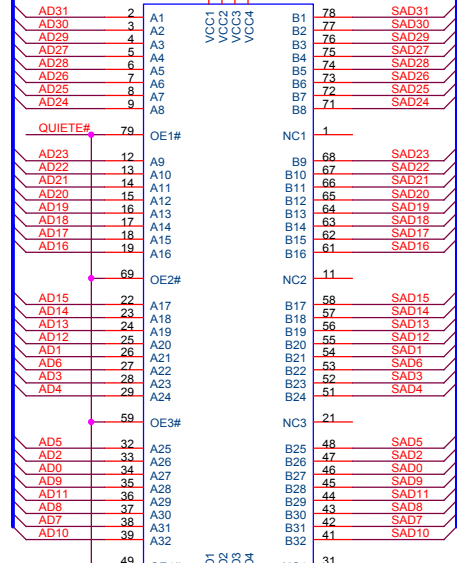


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File	FIR	Rev	3G
Size	Document Number JM2		
Date	星期五, 六月 06, 2003	Sheet	37 of 49

9,22,23,35 AD[0..31]



PI5C34X2245B

VCC_QBUF

C51 .1U C49 .1U C47 .047U C70 .047U

SAD[0..31]

D6

VCC_QBUFD

D8

+5VRUN

VCC_QBUF

QUIETE#

9,23 PIRQB#
22,23,25,35 PCI_PME#
9 GNT0#
9 PLOCK#
9,22,23,35 SERR#
9,22,23,35 IRDY#
9,22,23,35 C/BE3#
6,9,15,35 PCIRST#_1

AD24

A0 A1 A2 A3 A4 A5 A6 A7 A8 A9

B0 B1 B2 B3 B4 B5 B6 B7 B8 B9

SPIRQB#
D_IDSEL
SPME#
SGNT0#
SLOCK#
SSERR#
SIRDY#
SC/BE3#
SPCIRST#

9,22,23,35 PAR
9,22,23,35 FRAME#
9,22,23,35 C/BE1#
9,22,23,35 C/BE0#
9,22,23,35 PERR#
9,22,23,35 DEVSEL#
9,22,23,35 TRDY#
9,22,23,35 C/BE2#

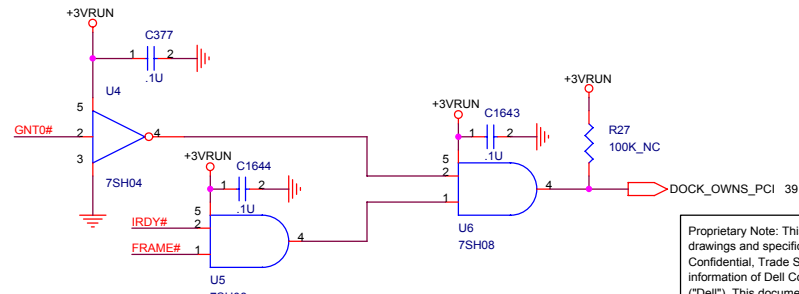
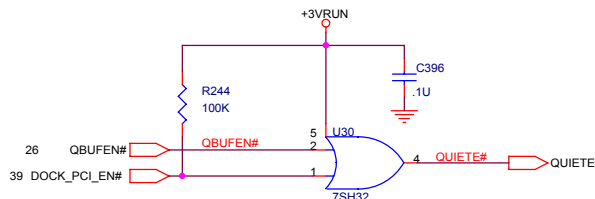
A10 A11 A12 A13 A14 A15 A16 A17 A18 A19

B10 B11 B12 B13 B14 B15 B16 B17 B18 B19

SPAR
SFRAME#
SC/BE1#
SC/BE0#
SPERR#
SDEVSEL#
STRDY#
SC/BE2#

PI5C162861

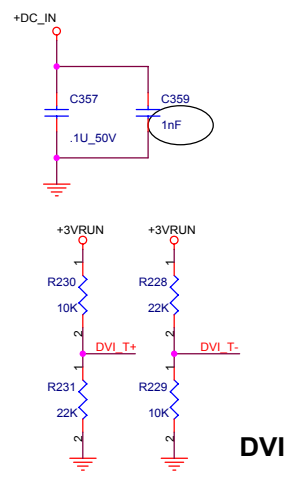
TSSOP48



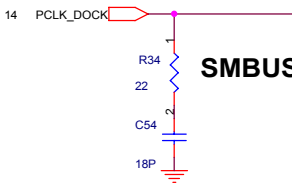
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File		Docking Q-SWITCH	
Size	Document Number	Rev	
	JM2	2E	
Date:	星期五, 六月 06, 2003	Sheet	38 of 49

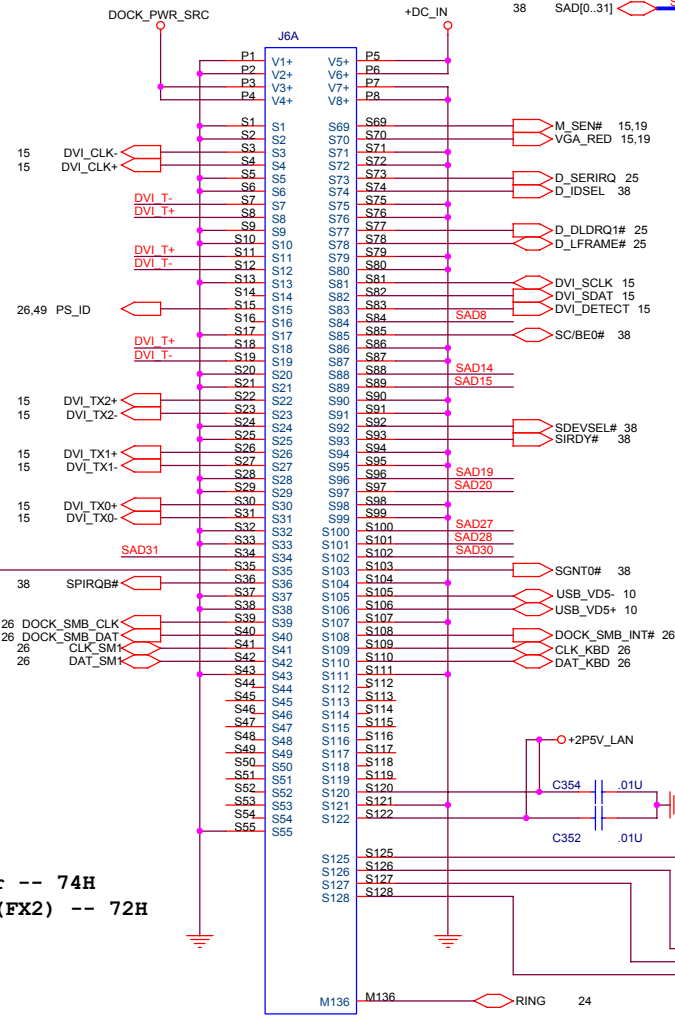


DVI

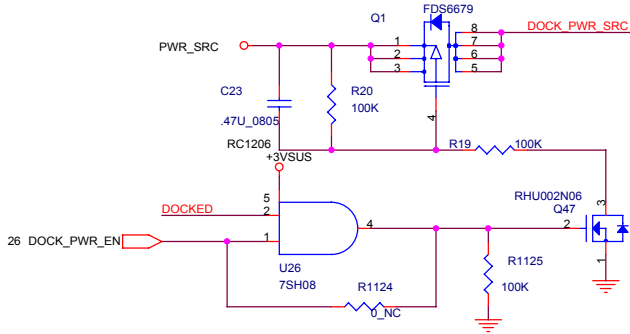


SMBUS

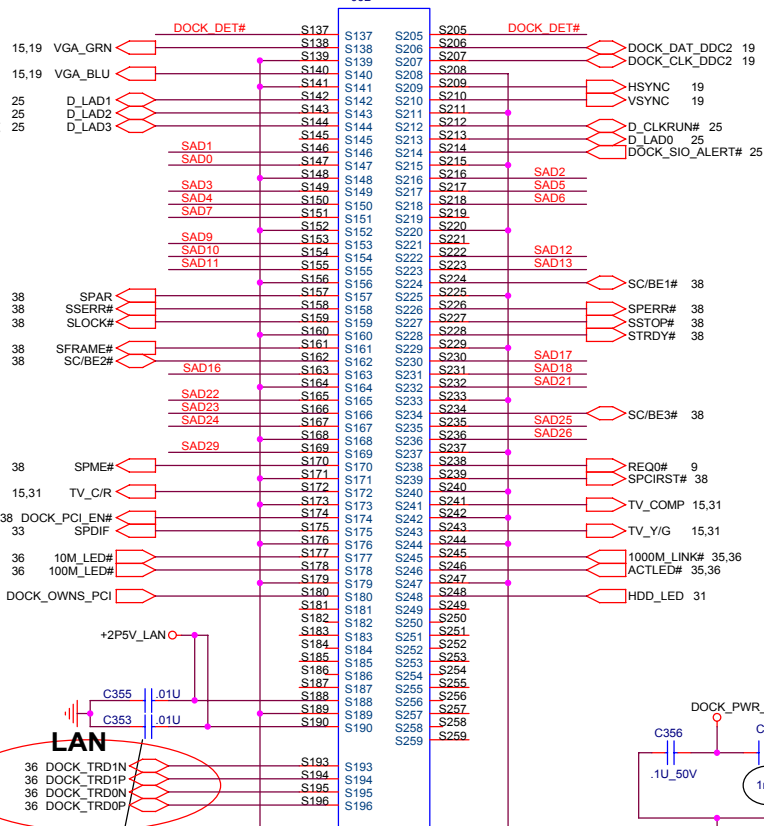
SMBUS ADDRESS :
DOCK/APR Microprocessor -- 74H
DOCK USB/IDE Interface(FX2) -- 72H



MODEM



LPC



SPDIF

USB



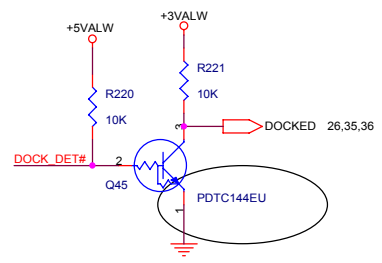
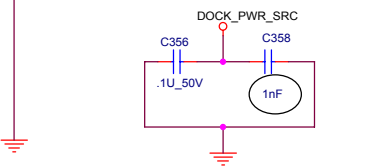
LAN



LAYOUT NOTES:
Terminators should be as close as possible to dock connector pins. Keep traces as short as possible.

VGA

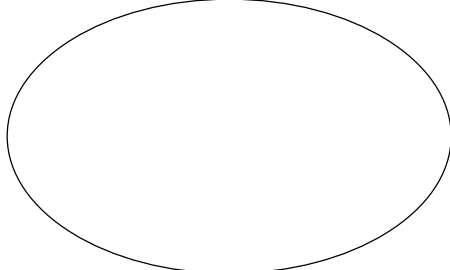
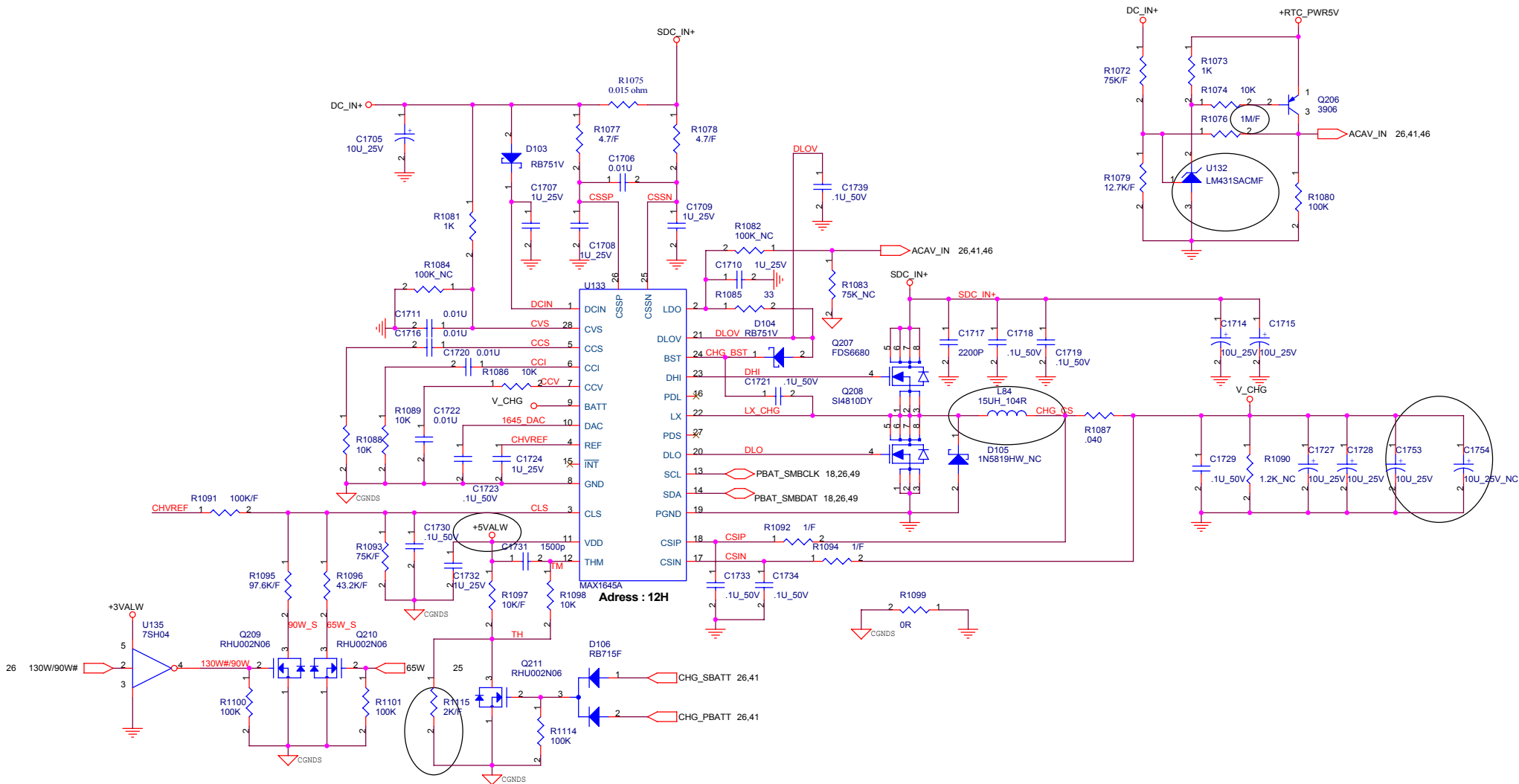
S-VIDEO



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DELL **QUANTA** **COMPUTER**

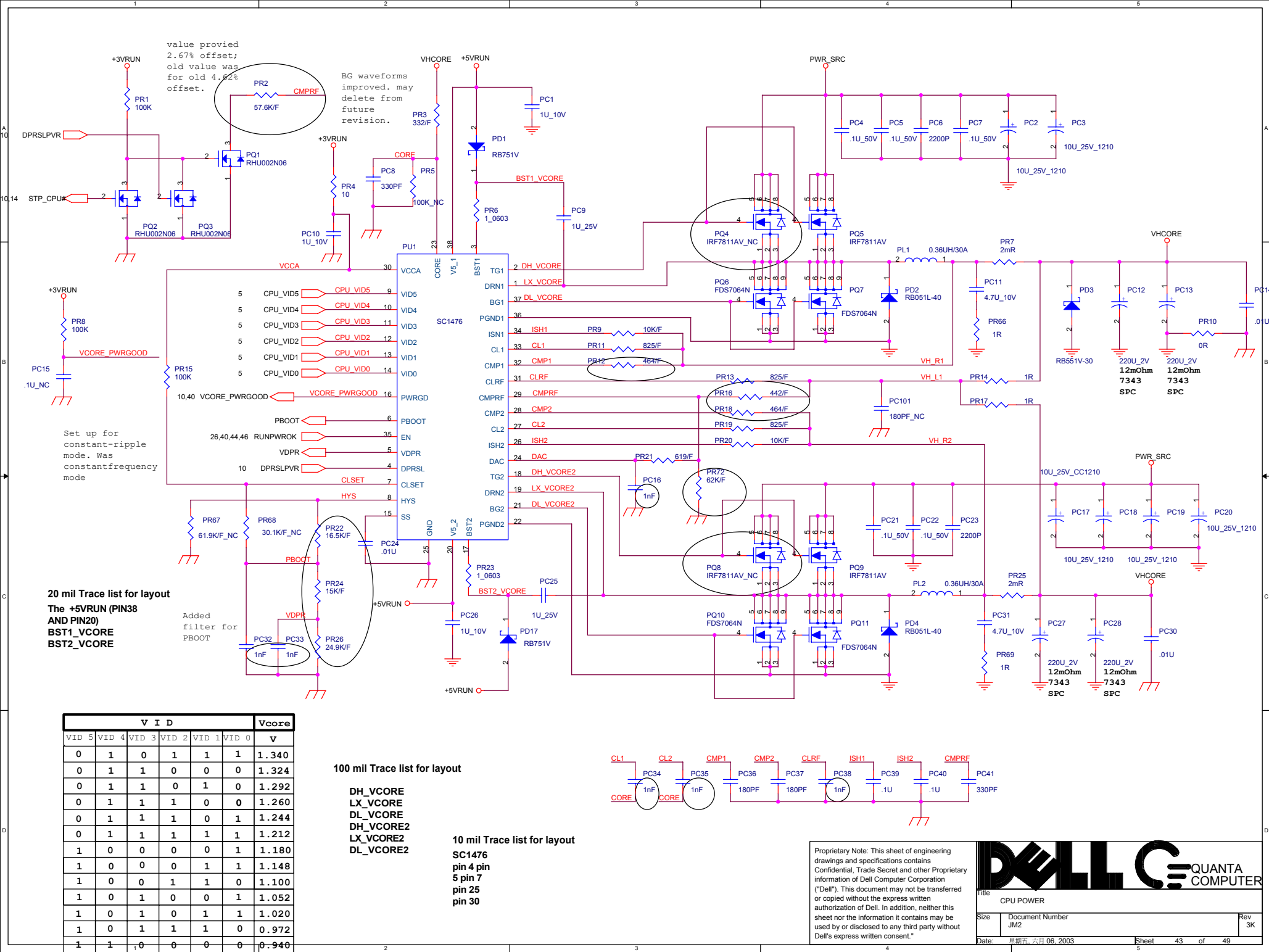
File Docking Station Conn.		
Size	Document Number JM2	Rev 3K
Date:	星期二, 六月 06, 2003	Sheet 39 of 49



	65W (2.48A)	90W (4.0A)	130W (5.8A)
65W	H	L	L
130W/90W#	H	L	H

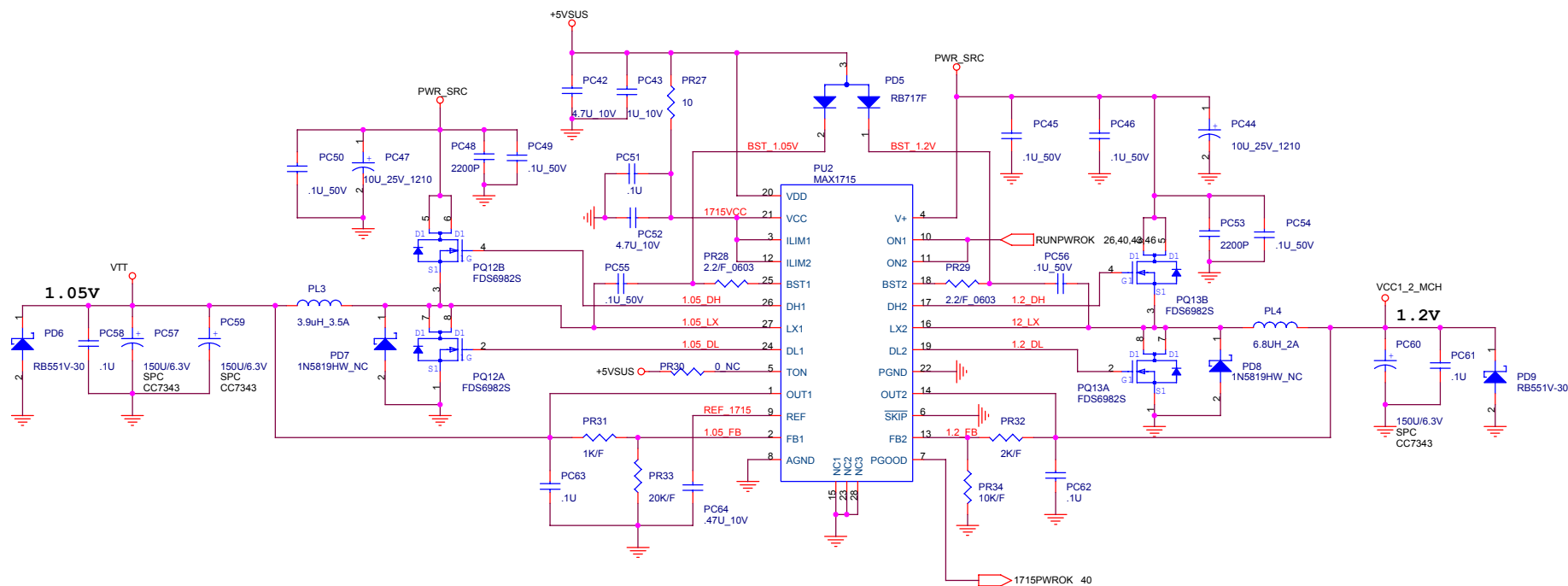
Proprietary Note: This sheet of engineering drawings and specifications contains Confidential, Trade Secret and other Proprietary information of Dell Computer Corporation ("Dell"). This document may not be transferred or copied without the express written authorization of Dell. In addition, neither this sheet nor the information it contains may be used by or disclosed to any third party without Dell's express written consent.

File Battery Charger		
Size	Document Number JM2	Rev 3F
Date:	星期五, 六月 06, 2003	Sheet 42 of 49



V I D						Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	1	0	1.292
0	1	1	1	0	0	1.260
0	1	1	1	0	1	1.244
0	1	1	1	1	1	1.212
1	0	0	0	0	1	1.180
1	0	0	0	1	1	1.148
1	0	0	1	1	0	1.100
1	0	1	0	0	1	1.052
1	0	1	0	1	1	1.020
1	0	1	1	1	0	0.972
1	1	1	0	0	0	0.940

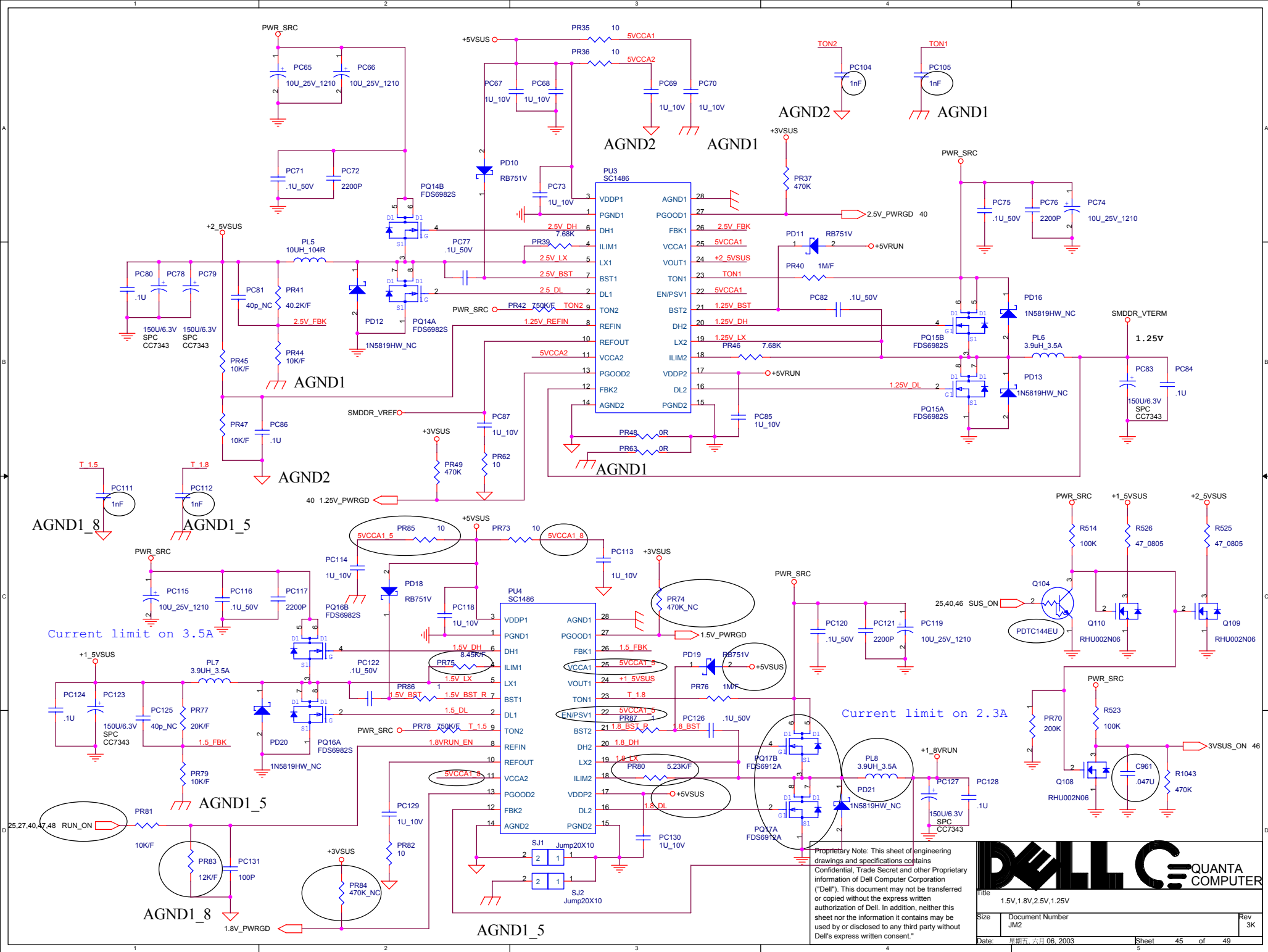
Title CPU POWER		
Size	Document Number JM2	Rev 3K
Date	2003年6月06日 Sheet 43 of 49	

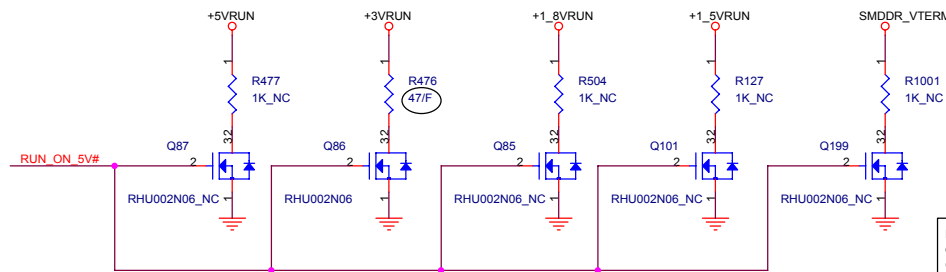
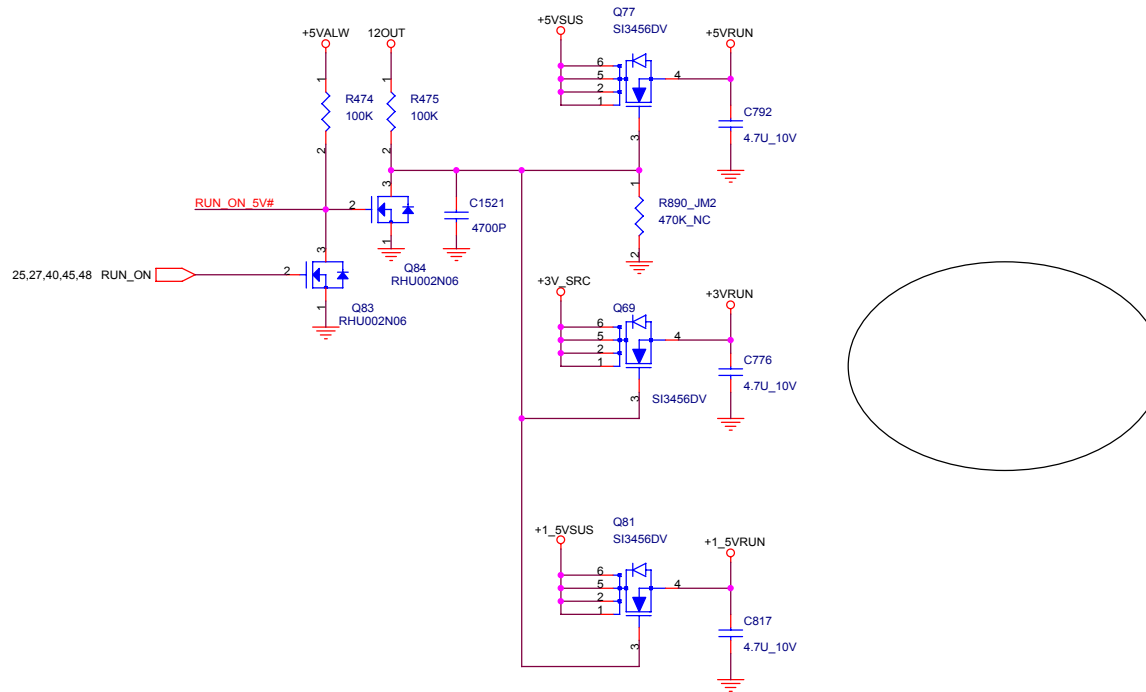


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DELL C QUANTA COMPUTER

Title VTT_1.05V,VCC1_2_MCH		Rev 1A
Size JM2	Document Number	
Date: 2003.06.06	Sheet 44	of 49





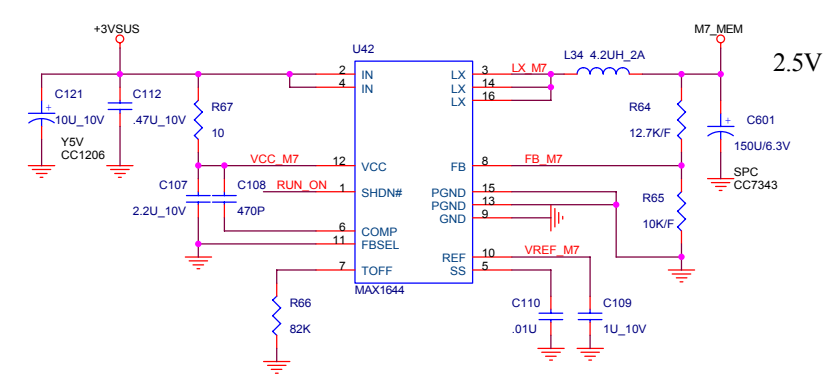
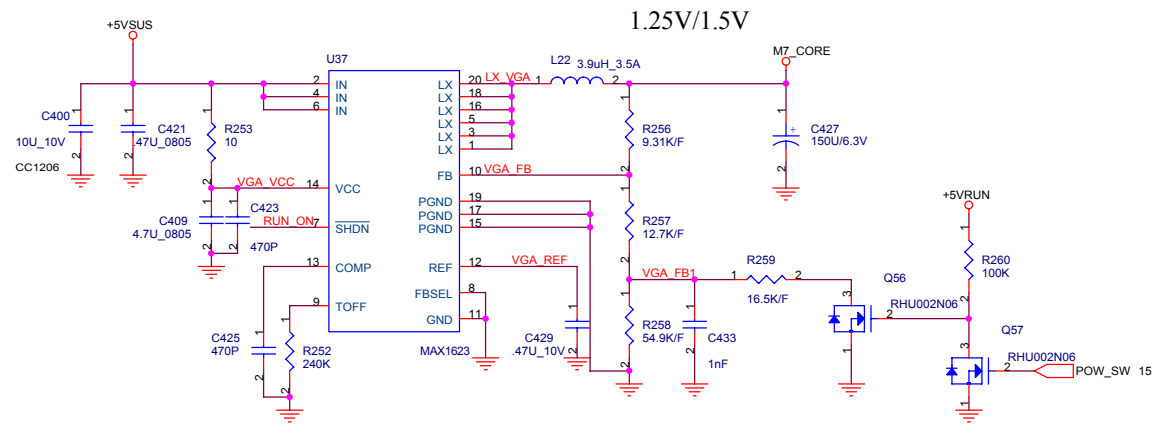
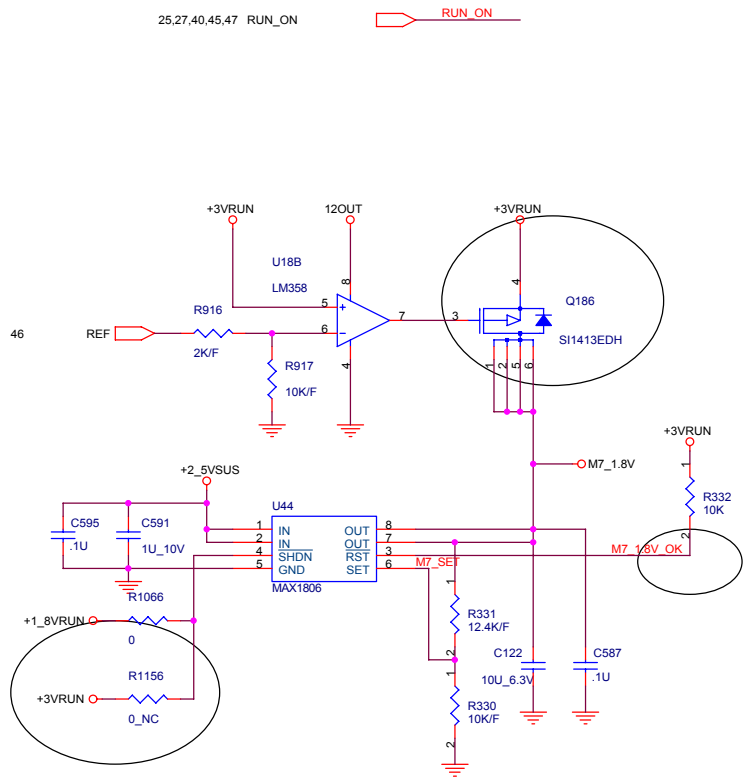
Reserve discharge path

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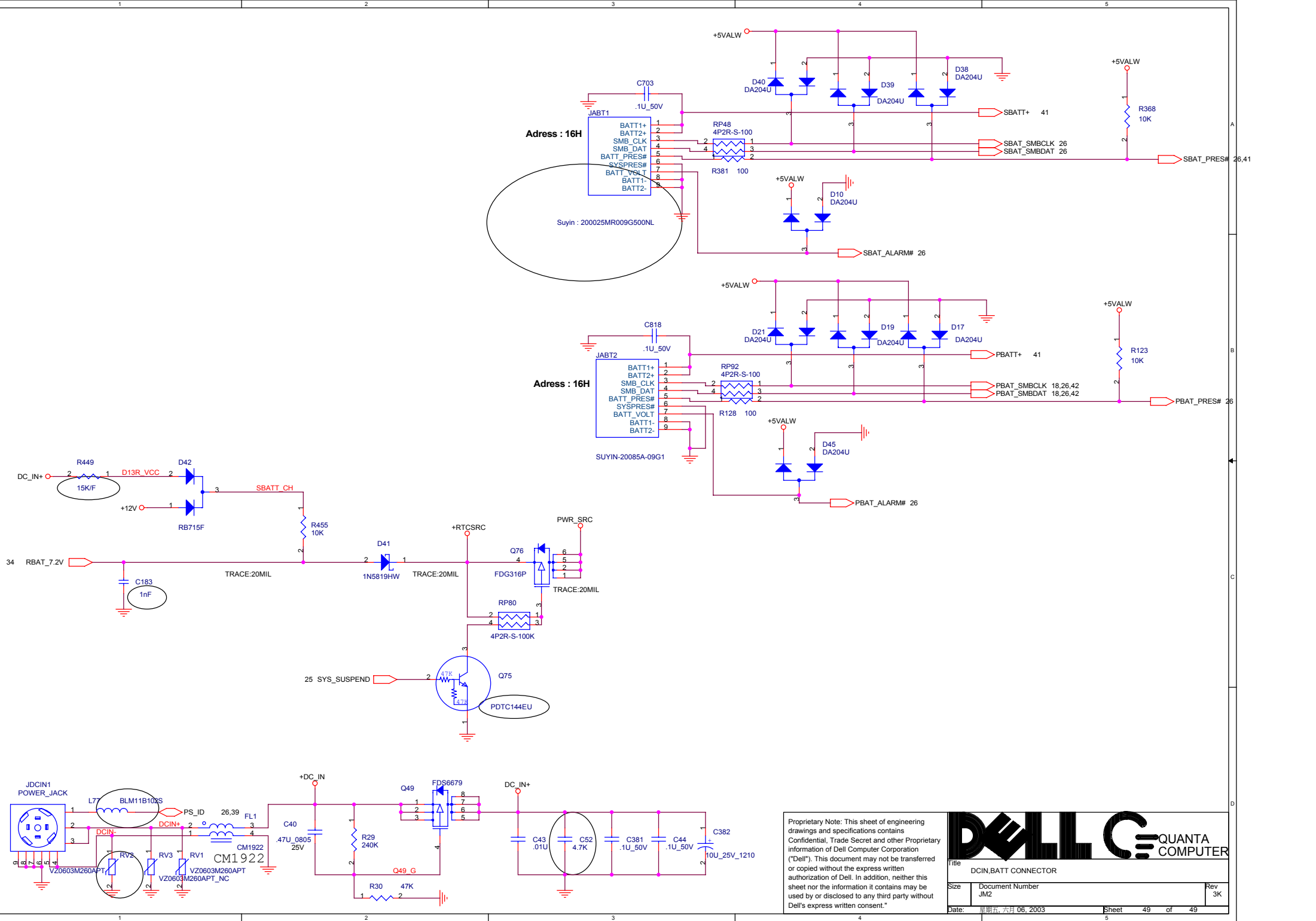
File	RUN POWER SW		
Size	Document Number	Rev	
	JM2	3K	
Date:	原明市, 六月 06, 2003	Sheet	47 of 49

POW_SW	M7D/M9 CSP 16 32
0	1.5V
1	1.25V



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File: VGA DC/DC	
Size: JM2	Document Number: JM2
Date: 2003.06.06	Sheet: 48 of 49



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File DCIN.BATT CONNECTOR		
Size JM2	Document Number	Rev 3K
Date: 2003.06.06	Sheet 49 of 49	

MODEL	REV	CHANGE LIST	Model	JM2	MB
			Page	FM	TO
JM2	1A	Preliminary Release	1	3I	
			2	1A	
	1B	Refer to ECN No. E200205-0561	3	1A	
			4	1A	3K
	2A	Refer to ECN No. E200206-0072	5	2E	3K
			6	3B	
	2B	Refer to ECN No. E200206-0273	7	3A	3K
			8	2E	
	2C	Refer to ECN No. E200207-0115	9	3C	
			10	3I	
	2D	Refer to ECN No. E200207-1038	11	3A	
			12	3F	
	2E	Refer to ECN No. E200207-1219, E200207-2119	13	1A	
			14	1A	
	3A	Refer to ECN No. E200208-1667, E200209-1836	15	3A	
			16	1A	
	3B	Refer to ECN No. E200210-0812, E200210-1418	17	3A	
			18	1A	
	3C	Refer to ECN No. E200210-2031, E200211-0167	19	3F	
			20	3F	3K
	3D	Refer to ECN No. E200211-0897	21	3A	
			22	3G	
	3E	Refer to ECN No. E200211-1860, E200211-2101, E200211-2472	23	3A	
			24	3A	
	3F	Refer to ECN No. E200211-3189	25	3D	
			26	3F	
	3G	Refer to ECN No. E200301-0228, E200301-1028	27	3F	3K
			28	3F	
	3H	Refer to ECN No. E200301-1742, E200301-2344, E200302-0074, E200302-0685	29	3A	
			30	3A	
	3I	Refer to ECN No. E200302-1751	31	3G	
			32	3A	3K
	3J	Refer to ECN No. E200302-2889	33	3C	3K
			34	3F	
	3K	Page 43 : De-pop PQ4 and PQ8 for save cost	35	3C	3K
		Page 43 : Change PR72 from 100K NC to 62K/F for correct schematic.	36	3G	3K
		Page 43 : Change PR2 from 22.6K/F to 57.6K/F for correct schematic.	37	3G	
		Page 27 : Change C34 from 1000P to 1nF for description same as BOM.	38	2E	
		Page 49 : Change C183 from 1000P to 1nF for description same as BOM.	39	2E	3K
		Page 20 : Change C337 from 1000P to 1nF for description same as BOM.	40	1A	
		Page 39 : Change C358, C359 from 1000P to 1nF for description same as BOM.	41	3F	
		Page 33 : Change C1589 from 1000P to 1nF for description same as BOM.	42	3F	
		Page 43 : Change PC16, PC32, PC33, PC34, PC35, PC38 from 1000P to 1nF for description same as BOM.	43	3C	3K
		Page 45 : Change PC104, PC105, PC111, PC112 from 1000PF to 1nF for description same as BOM.	44	1A	
		Page 20 : Change Q30, Q62 from DTC144EUA to PDTC144EU for description same as BOM.	45	3D	3K
		Page 32 : Change Q19, Q212 from DTC144EUA to PDTC144EU for description same as BOM.	46	3H	3K
		Page 36 : Change Q204, Q205 from DTC144EUA to PDTC144EU for description same as BOM.	47	3C	3K
		Page 39 : Change Q45 from DTC144EUA to PDTC144EU for description same as BOM.	48	3F	
		Page 45 : Change Q104 from DTC144EUA to PDTC144EU for description same as BOM.	49	3F	3K
		Page 46 : Change Q36 from DTC144EUA to PDTC144EU for description same as BOM.			
 QUANTA COMPUTER		PROJECT : JM2	DOC. NO. 204	REV: 3K	ASSY: 31JM2MB0036
		APPROVED BY : Eric Liu	CHECKED BY:Kevin Chien	DRAWN BY : Lit Wu	DATE : JUN. 06, 2003
				SHEET 1 OF 1	

MODEL		REV	CHANGE LIST				Model			
							Page	JM2	MB	
								FM	TO	
QUANTA COMPUTER	JM2	3K	Page 49 : Change Q75 from DTC144EUA to PDTC144EU for description same as BOM. Page 5 : Change C996, C998, C1000, C1002, C1003, C1005, C1006, C1007, C1008, C1009, C1010, C1011, C1012, C1013, C1014, C1015, C1018, C1040, C1041, C1042, C1043, C1044, C1049, C1050, C1051, C1052, C1053, C995, C997, C999, C1001, C1004, C1016, C1017, C1019 from 10U_6.3V to 10U_4V for correct schematic. Page 7 : Change C1527 from 10U_6.3V to 10U_4V for correct schematic. Page 36 : Change L85~L92 from MLK1005S15NJ to MLK1005S15NJ(0.015u) for description clear with BOM. Page 36 : Change PV32 from PAD126X98 to PAD138X98 for correct schematic. Page 35 : Change RP215 from 4P2R-S-1K to 4P2R-S-1K/F for description same as BOM. Page 4 : Change R559 from 1.5K to 1.5K/F for description same as BOM. Page 49 : Change R449 from 15K to 15K/F for description same as BOM. Page 47 : Change R476 from 47 to 47/F for description same as BOM. Page 36 : Change U129 from PI3L301 to PI3L301DA.				1	3I		
							2	1A		
							3	1A		
							4	1A	3K	
							5	2E	3K	
							6	3B		
							7	3A	3K	
							8	2E		
							9	3C		
							10	3I		
							11	3A		
							12	3F		
							13	1A		
							14	1A		
							15	3A		
							16	1A		
							17	3A		
							18	1A		
							19	3F		
							20	3F	3K	
							21	3A		
							22	3G		
							23	3A		
							24	3A		
							25	3D		
							26	3F		
							27	3F	3K	
							28	3F		
							29	3A		
							30	3A		
							31	3G		
							32	3A	3K	
							33	3C	3K	
							34	3F		
							35	3C	3K	
							36	3G	3K	
							37	3G		
							38	2E		
							39	2E	3K	
							40	1A		
							41	3F		
							42	3F		
							43	3C	3K	
							44	1A		
							45	3D	3K	
							46	3H	3K	
							47	3C	3K	
							48	3F		
							49	3F	3K	
			PROJECT : JM2		DOC. NO. 204		REV: 3K		ASSY: 31JM2MB0036	
			APPROVED BY : Eric Liu		CHECKED BY:Kevin Chien		DRAWN BY : Lit Wu		DATE : JUN. 06, 2003 SHEET 1 OF 1	